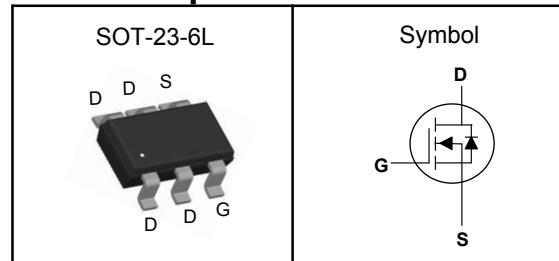


N-Channel Enhancement Mode MOSFET

Features

- Low $R_{DS(on)}$ for low conduction loss
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

Pin Description



Applications

- Power Management in Desktop Computer
- DC/DC Converters

V_{DSS}	110	V
$R_{DS(ON)-Typ}$	260	$\text{m}\Omega$
I_D	3	A

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V_{DSS}	Drain-Source Voltage	110	V
V_{GSS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{①}$	Pulse Drain Current Tested	10	A
I_D	Continuous Drain Current	3	A
P_D	Maximum Power Dissipation	1.25	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{θJA}$	Thermal Resistance-Junction to Ambient($t \leq 10\text{s}$)	62.5	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in^2 FR-4 board with 1oz.



N-Channel Enhancement Mode MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

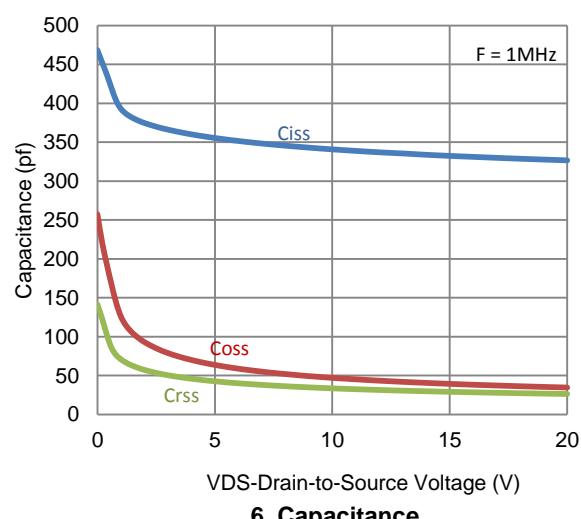
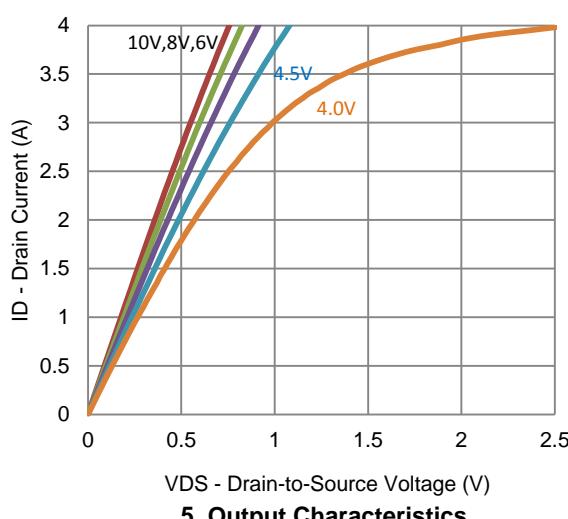
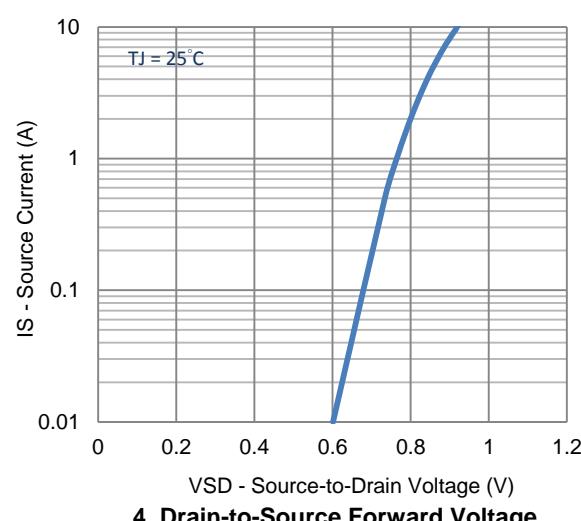
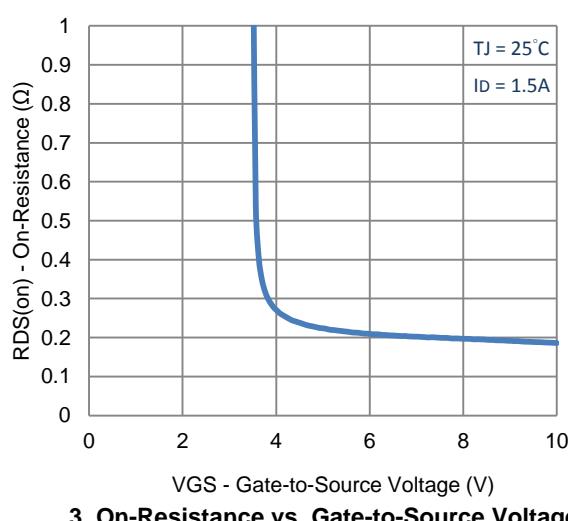
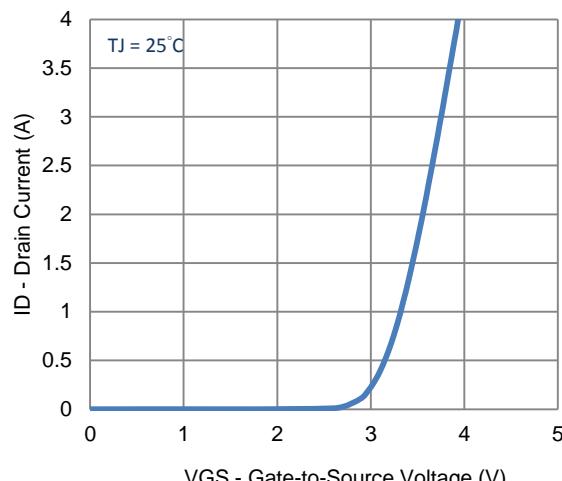
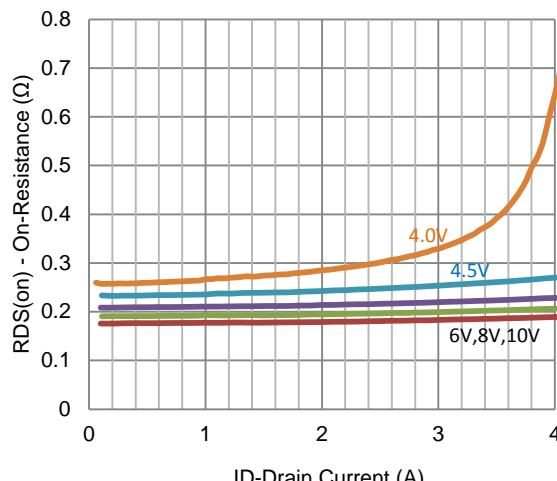
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	110	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	μA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1	---	2.5	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=3\text{A}$	---	260	280	$\text{m}\Omega$
Dynamic Characteristics^⑤						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=15\text{V}$, Freq.=1MHz	---	332	---	pF
C_{oss}	Output Capacitance		---	40	---	
C_{rss}	Reverse Transfer Capacitance		---	29	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_{\text{G}}=6\Omega$, $I_{\text{D}}=1.5\text{A}$, $R_{\text{L}}=33.3\Omega$	---	5.7	---	nS
T_{r}	Turn-on Rise Time		---	4.3	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	12.8	---	
T_{f}	Turn-off Fall Time		---	4.4	---	
Q_{g}	Total Gate Charge	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=1.5\text{A}$	---	4.1	---	nC
Q_{gs}	Gate-Source Charge		---	1.4	---	
Q_{gd}	Gate-Drain Charge		---	1.9	---	
Source-Drain Characteristics ($T_J=25^\circ\text{C}$)						
$V_{\text{SD}}^{④}$	Diode Forward Voltage	$I_{\text{S}}=1\text{A}$, $V_{\text{GS}}=0\text{V}$	---	---	1.2	V

Note ④ : Pulse test (pulse width≤300us, duty cycle≤2%).

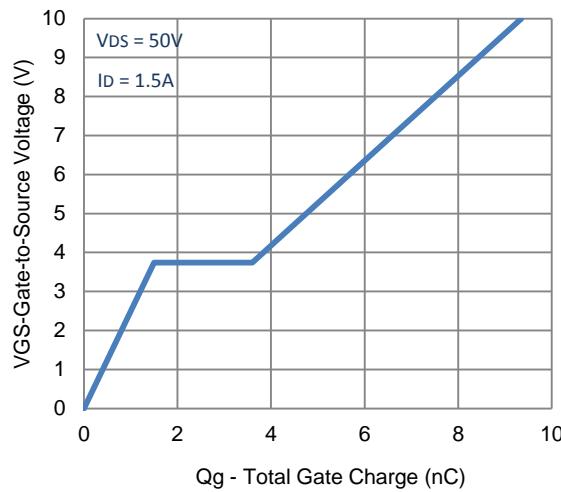
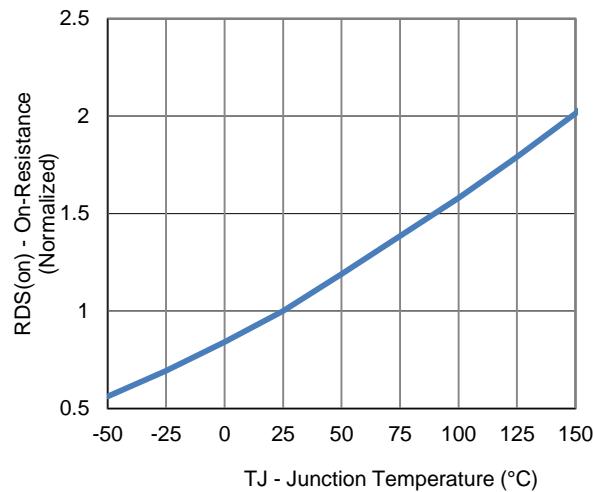
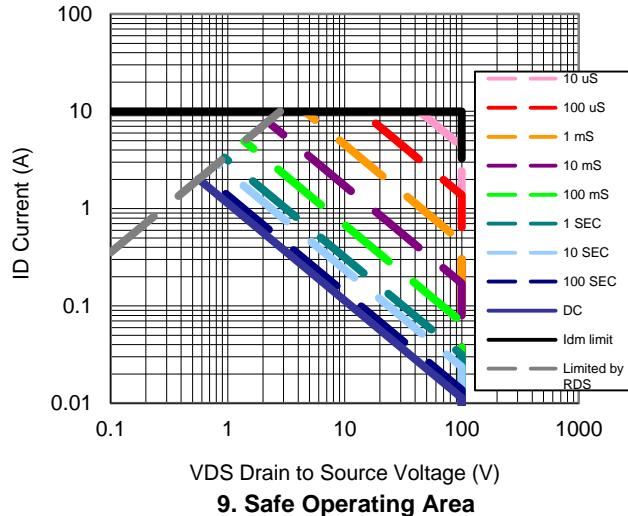
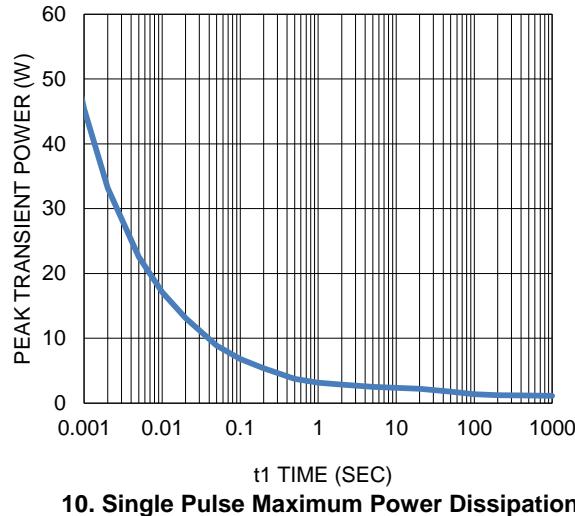
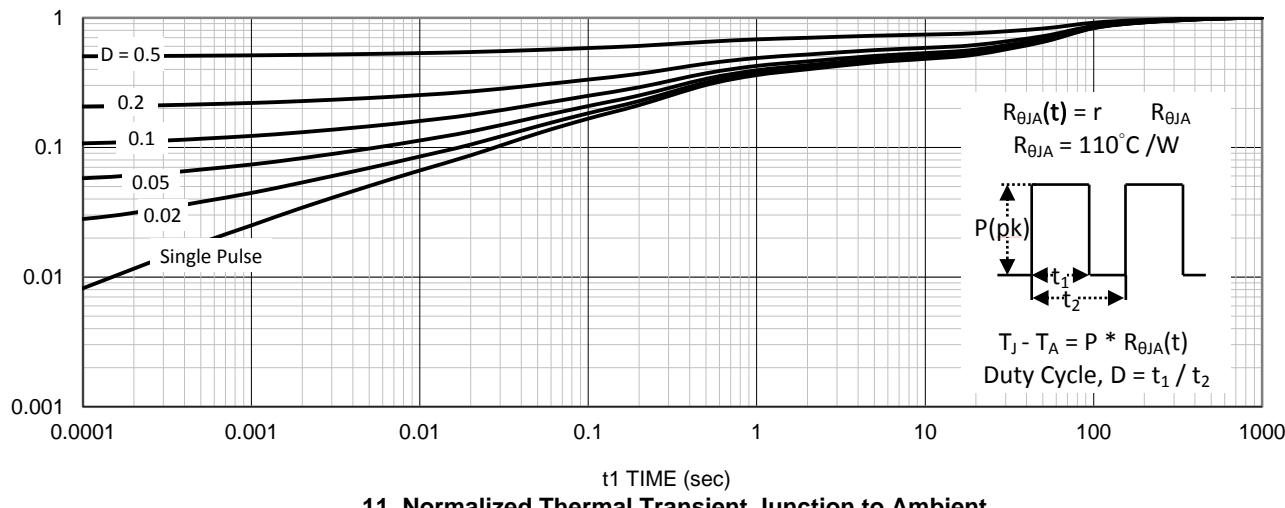
Note ⑤ : Guaranteed by design, not subject to production testing.

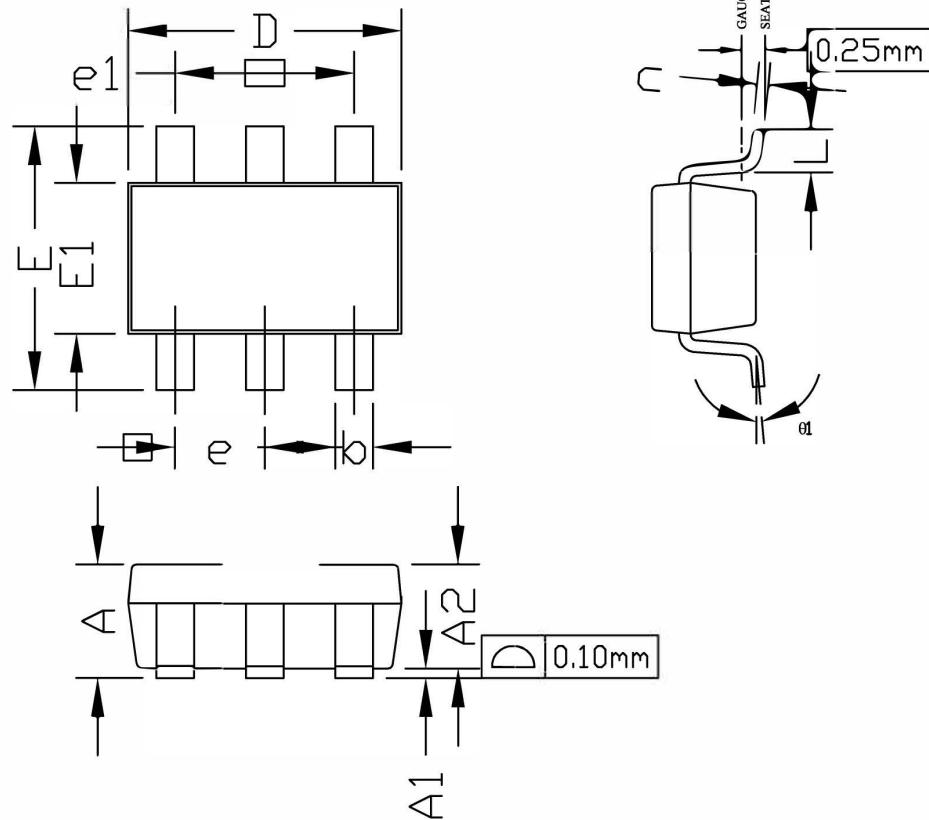
N-Channel Enhancement Mode MOSFET

Typical Characteristics



N-Channel Enhancement Mode MOSFET


7. Gate Charge

8. Normalized On-Resistance Vs Junction Temperature

9. Safe Operating Area

10. Single Pulse Maximum Power Dissipation

11. Normalized Thermal Transient Junction to Ambient

N-Channel Enhancement Mode MOSFET
SOT23-6L Package Outline Dimensions


Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	0.80	1.00	1.25	E	2.50	2.80	3.10
A1	0.00	---	0.15	E1	1.50	1.60	1.70
A2	0.80	1.10	1.20	e	0.95 REF		
b	0.25	0.35	0.45	e1	1.90 REF		
c	0.08	0.13	0.20	L	0.30	0.45	0.60
D	2.70	2.90	3.10	θ1	0°		8°