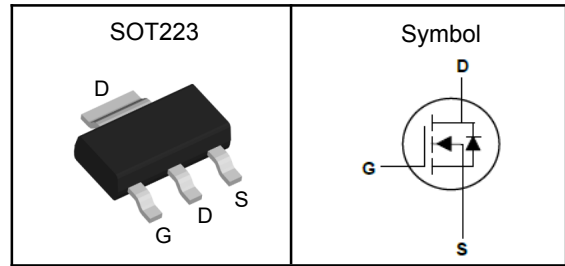


N-Channel Enhancement Mode MOSFET
Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description


V_{DSS}	100	V
$R_{DS(ON)-Typ}$	38	m Ω
I_D	6	A

Absolute Maximum Ratings($T_A=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V_{DSS}	Drain-Source Voltage	100	V
V_{GSS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{①}$	Pulse Drain Current Tested	24	A
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$ 6	A
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$ 1.5	W
EAS	Single Pulse Avalanche Energy	11.3	mJ
I_{AS}	Avalanche Current	15	A

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ^① (Max)	70	$^\circ\text{C/W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^\circ\text{C}$.

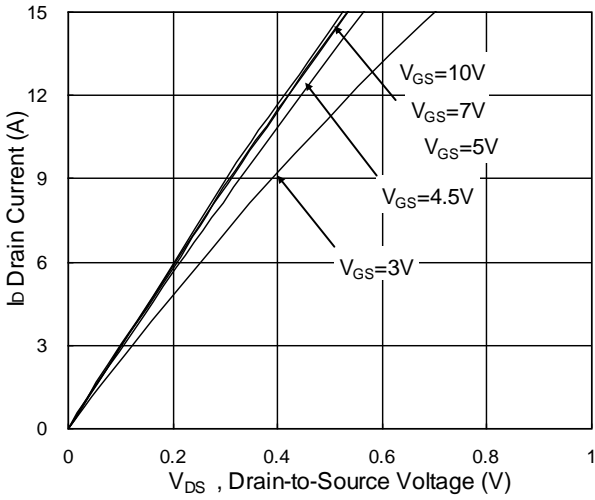
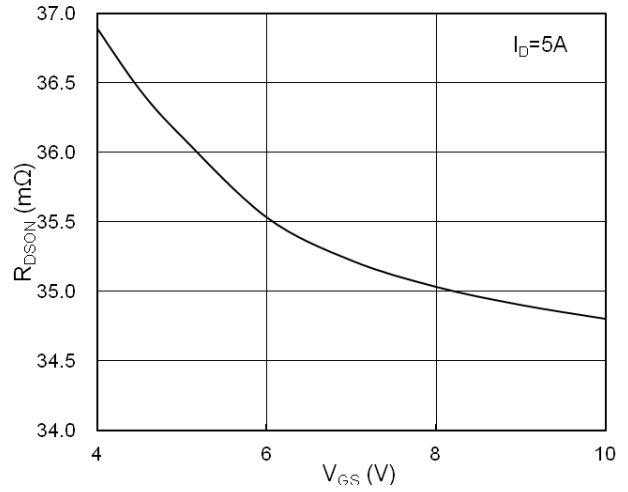
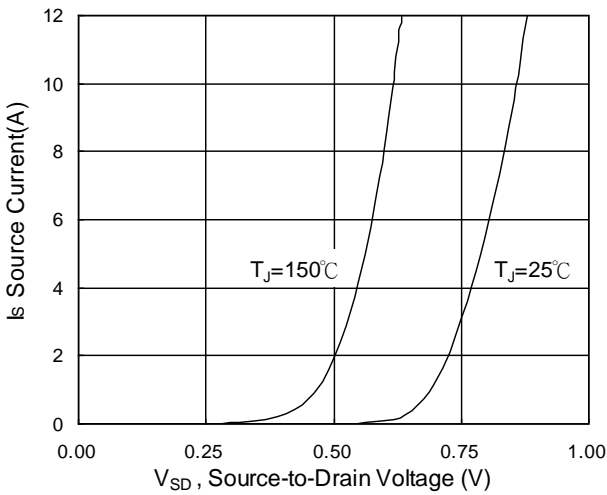
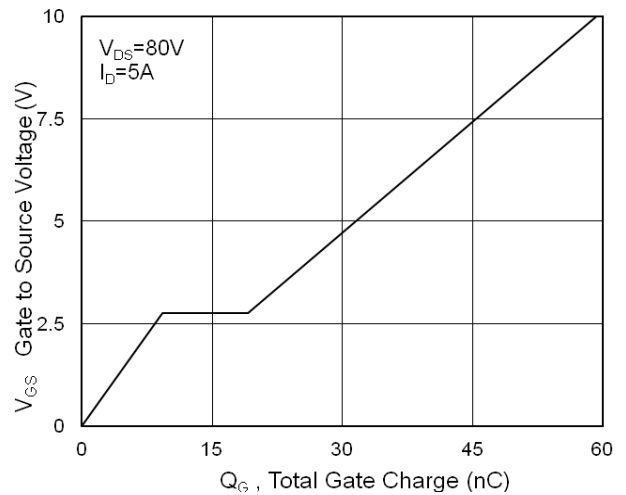
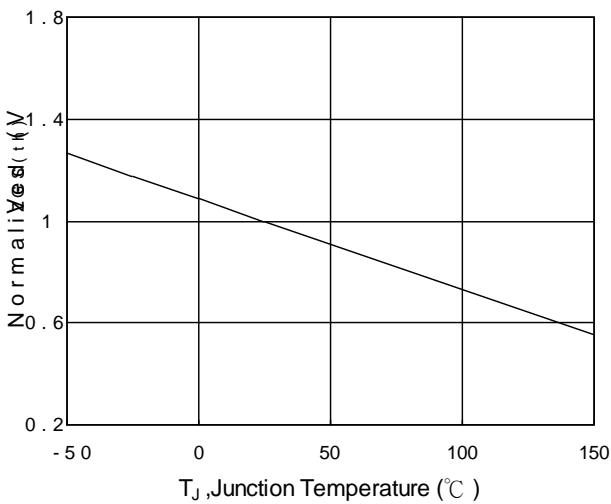
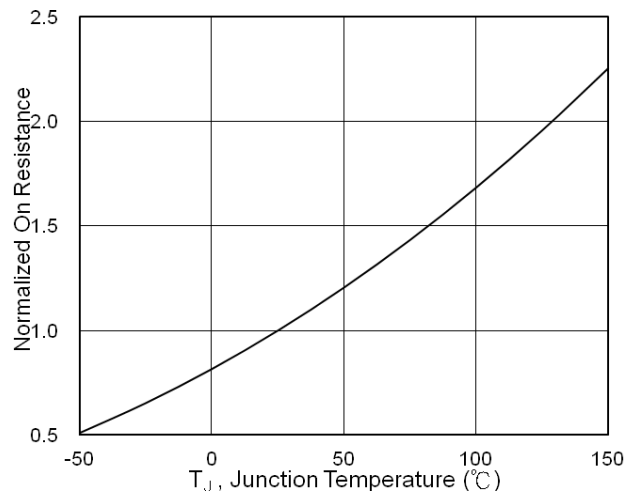
Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.

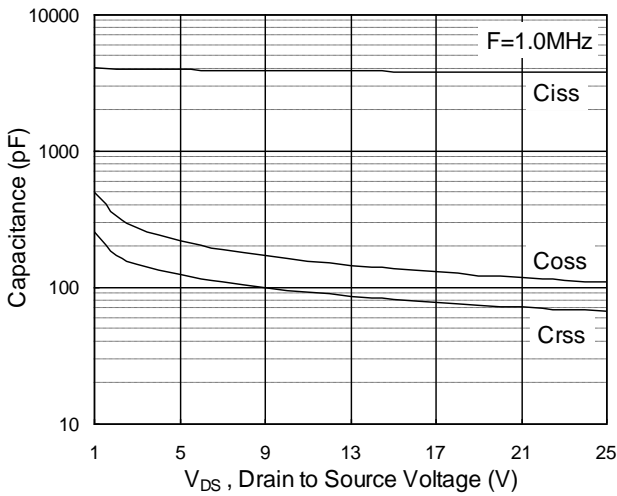
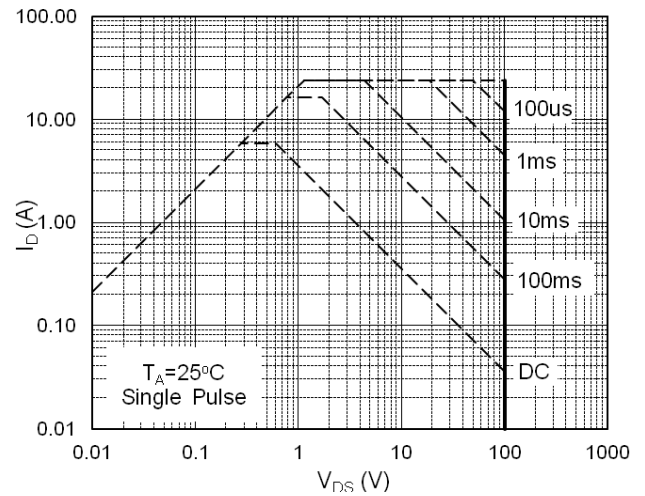
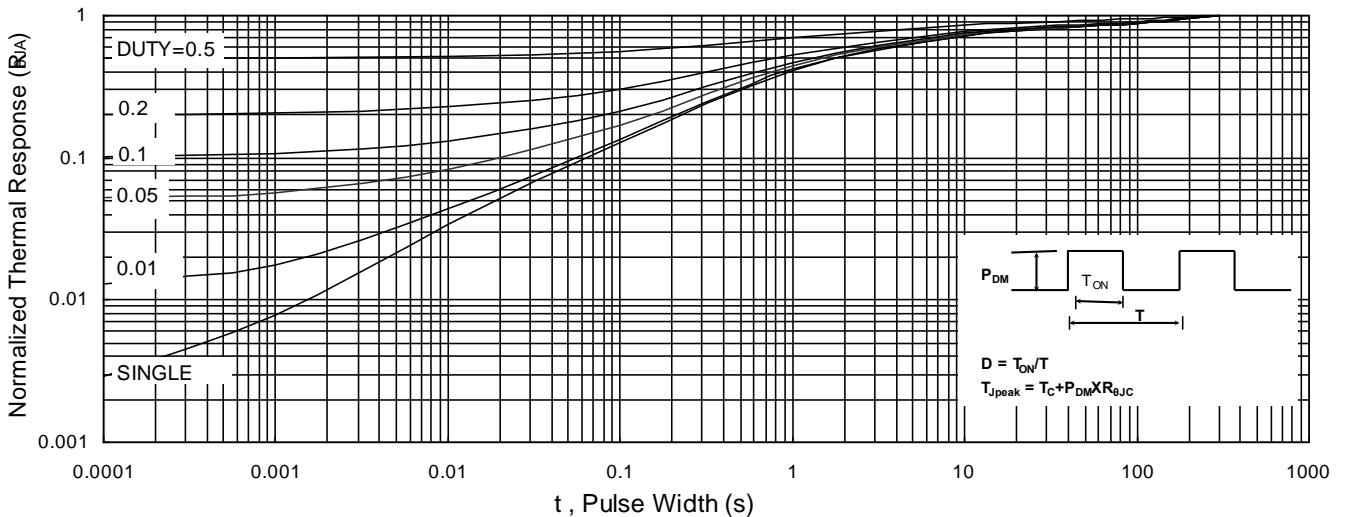
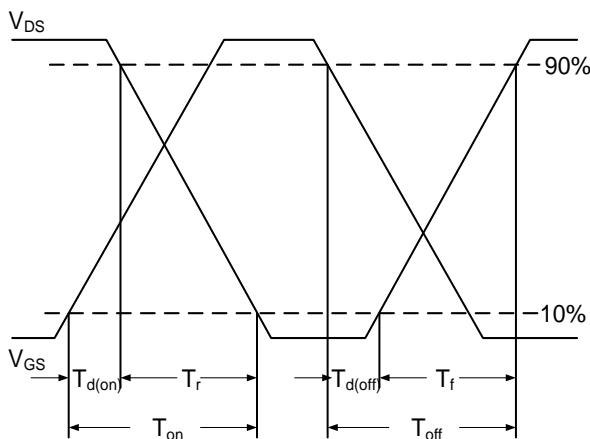
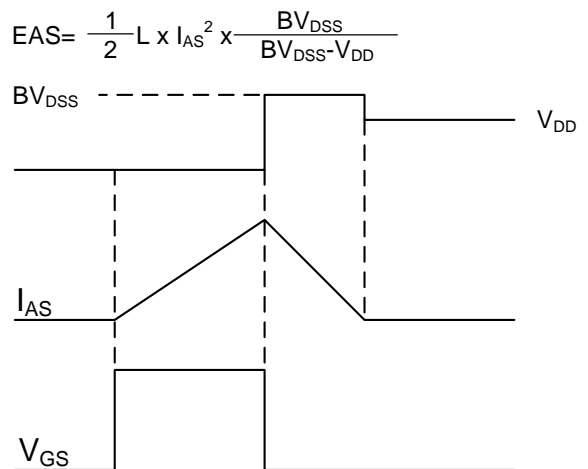
**N-Channel Enhancement Mode MOSFET****Electrical Characteristics** ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

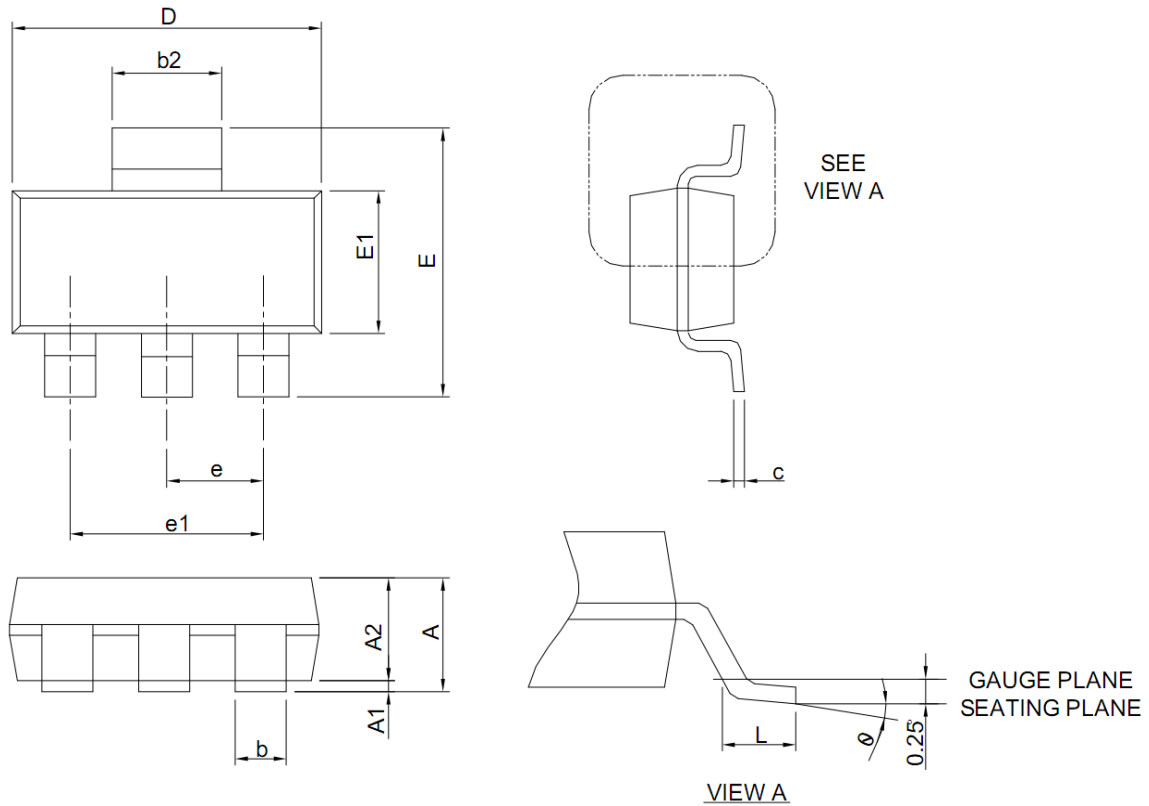
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=80V, V_{GS}=0V$	---	---	1	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	---	2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=5A$	---	38	47	m Ω
		$V_{GS}=4.5V, I_D=5A$	---	40	50	
Dynamic Characteristics ^⑤						
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=15V,$ Freq.=1MHz	---	3848	---	pF
C_{oss}	Output Capacitance		---	137	---	
C_{rss}	Reverse Transfer Capacitance		---	82	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=50V,$ $V_{GS}=10V, R_G=3.3\Omega,$ $I_D=3A$	---	10.8	---	nS
T_r	Turn-on Rise Time		---	27	---	
$T_{d(off)}$	Turn-off Delay Time		---	56	---	
T_f	Turn-off Fall Time		---	24	---	
Q_g	Total Gate Charge	$V_{DS}=80V, V_{GS}=10V,$ $I_D=5A$	---	60	---	nC
Q_{gs}	Gate-Source Charge		---	9.2	---	
Q_{gd}	Gate-Drain Charge		---	9.9	---	
Source-Drain Characteristics ($T_J=25^\circ\text{C}$)						
V_{SD}	Diode Forward Voltage _z	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=5A,$ $di/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	25	---	nS
Q_{rr}	Reverse Recovery Charge		---	29	---	nC

Note ④ : Pulse test (pulse width \leq 300 μs , duty cycle \leq 2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Enhancement Mode MOSFET
Typical Characteristics

Fig.1 Typical Output Characteristics

Fig.2 On-Resistance vs. Gate-Source

Fig.3 Forward Characteristics Of Reverse

Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ vs. T_J

Fig.6 Normalized R_{DSON} vs. T_J

N-Channel Enhancement Mode MOSFET

Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Switching Waveform

N-Channel Enhancement Mode MOSFET
SOT223 Package Outline Data


Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	1.50	1.65	1.80	A1	0.02	0.06	0.10
A2	1.50	1.60	1.70	b	0.66	0.72	0.80
b2	2.90	3.00	3.10	c	0.23	0.30	0.35
D	6.30	6.50	6.70	E	6.70	7.00	7.30
E1	3.30	3.50	3.70	e	2.30 REF		
e1	4.60 REF			L	0.75	--	1.15
θ	0°	--	10°				