

N-Channel Enhancement Mode MOSFET

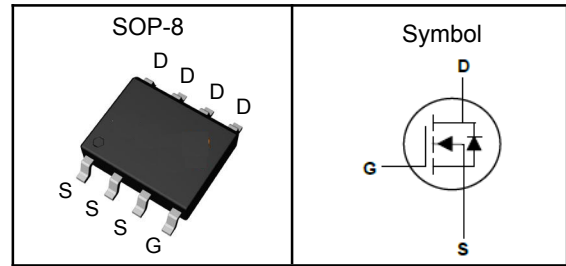
Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description



V_{DSS}	100	V
$R_{DS(ON)-Typ}$	16	m Ω
I_D	8.5	A

Absolute Maximum Ratings ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit	
V_{DSS}	Drain-Source Voltage	100	V	
V_{GSS}	Gate-Source Voltage	± 20	V	
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
E_{AS}	Single Pulse Avalanche Energy ³	16	mJ	
$I_{DM}^{①}$	Pulse Drain Current Tested	40	A	
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	8.5	A
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	2.5	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹ (Max)	85	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in^2 FR-4 board with 1oz.



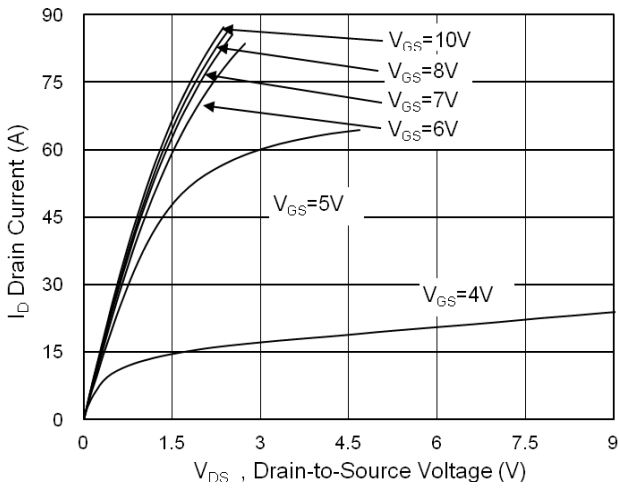
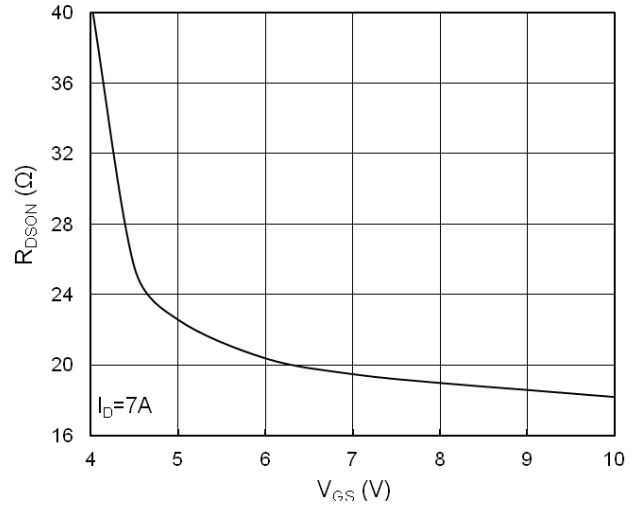
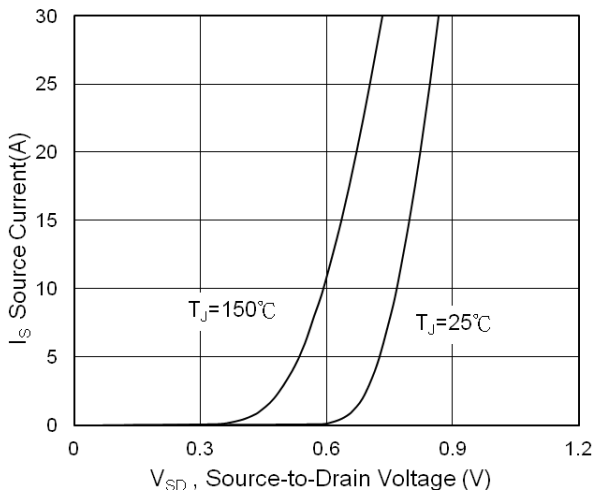
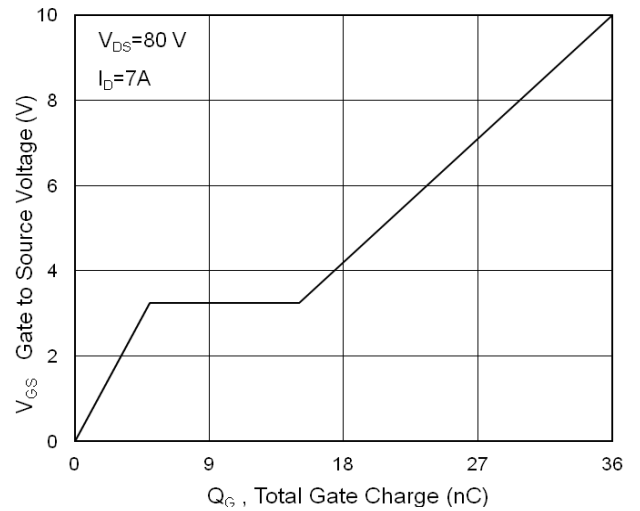
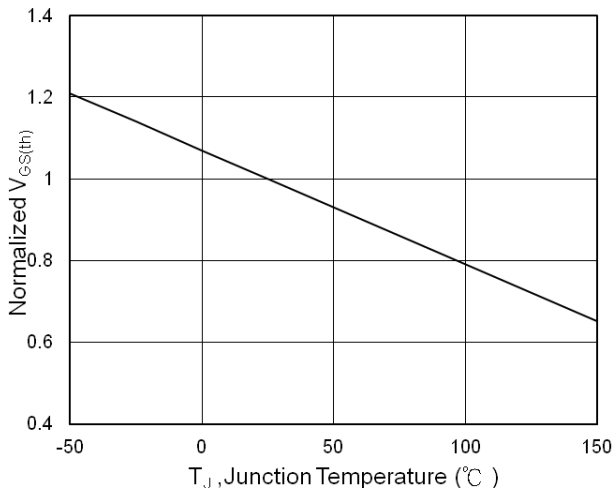
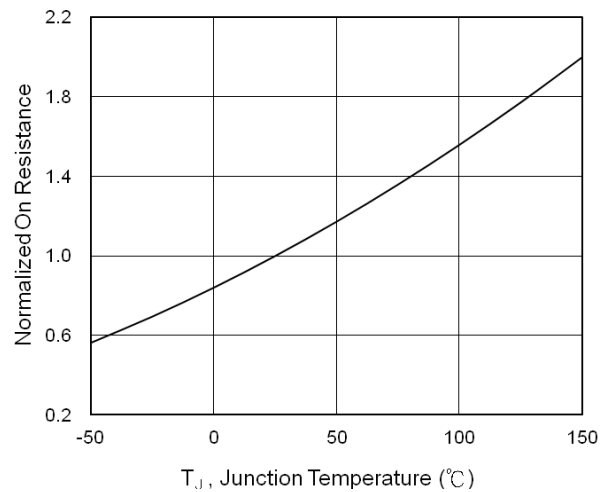
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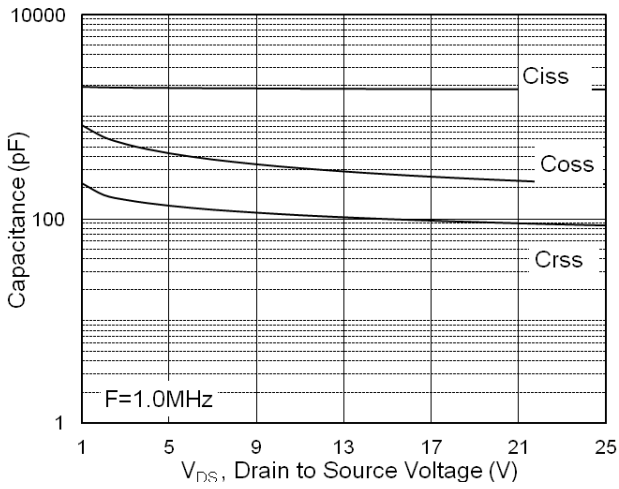
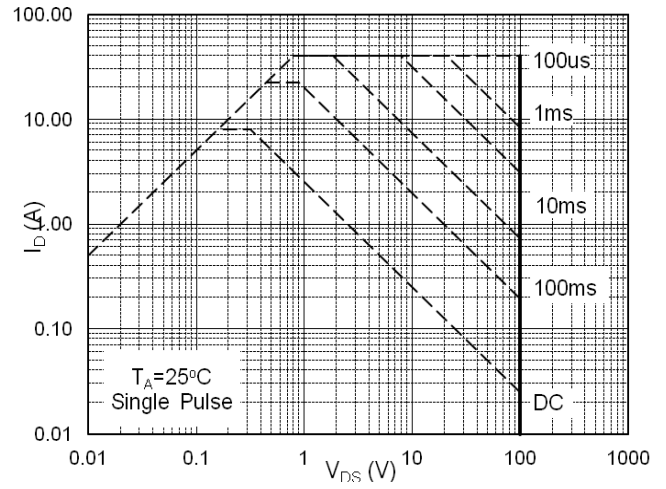
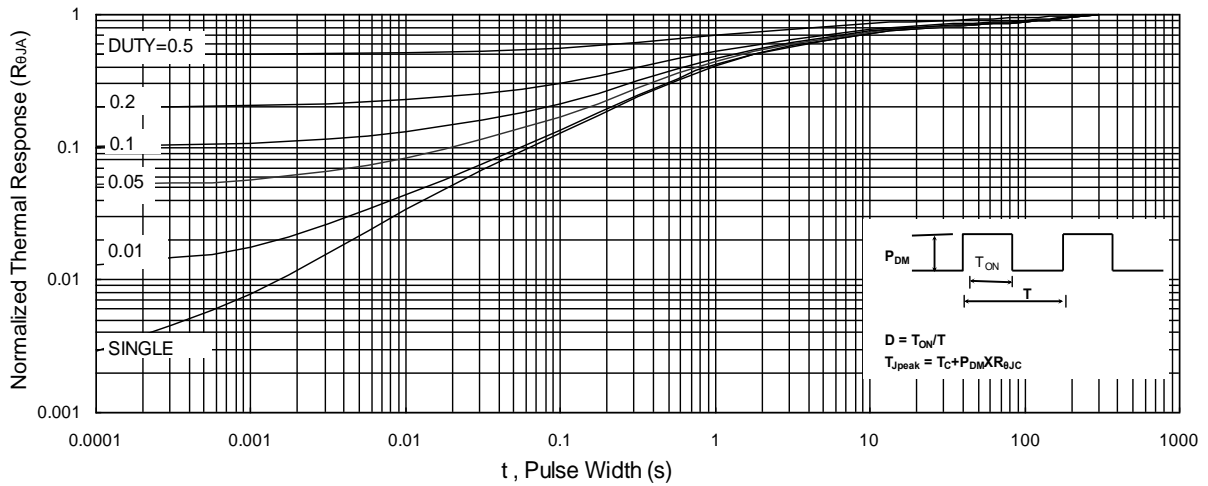
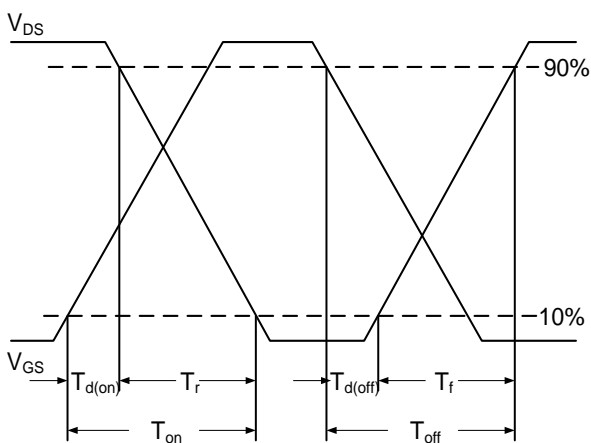
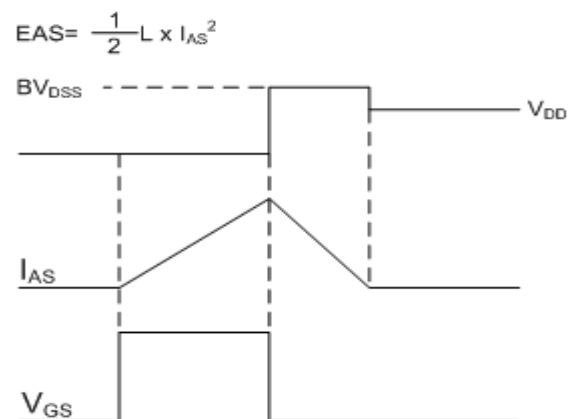
Electrical Characteristics ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

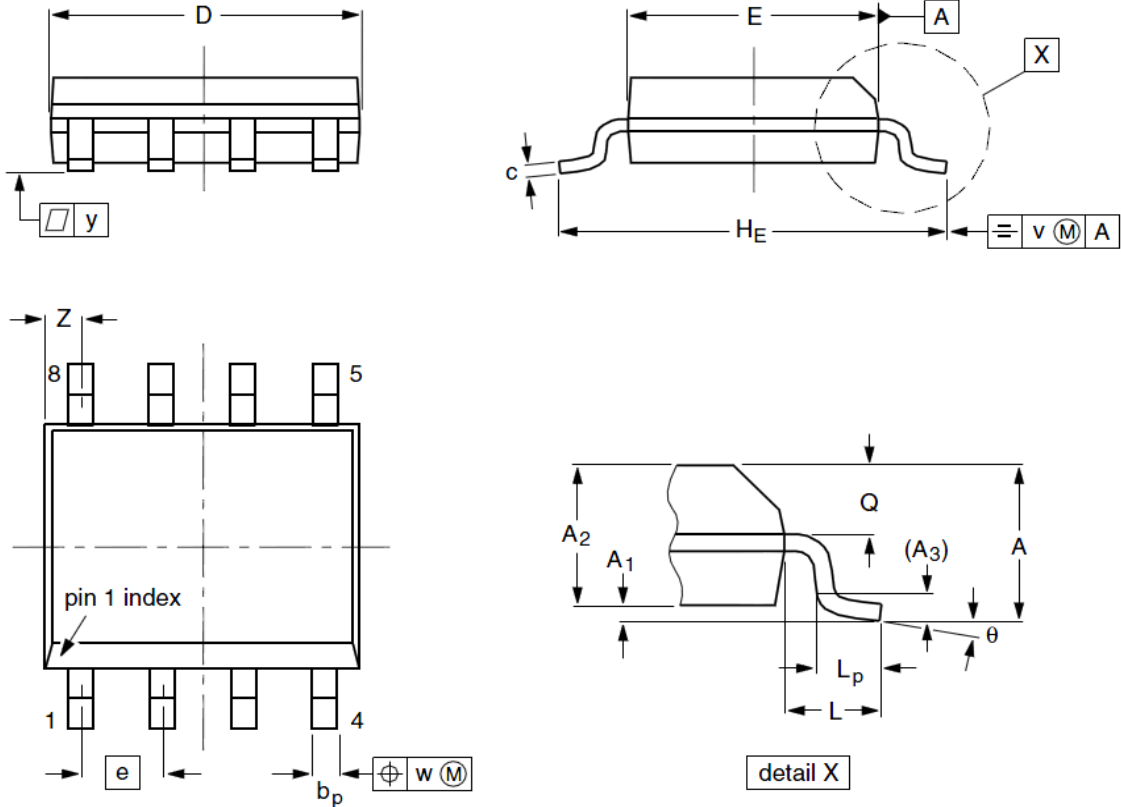
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=80V, V_{GS}=0V$	---	---	10	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	---	2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=7A$	---	16	20	$m\Omega$
		$V_{GS}=4.5V, I_D=5A$	---	19	25	$m\Omega$
Dynamic Characteristics ^⑤						
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, \text{Freq.}=1\text{MHz}$	---	1930	---	pF
C_{oss}	Output Capacitance		---	245	---	
C_{rss}	Reverse Transfer Capacitance		---	125	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=50V, V_{GS}=10V, R_G=3.3\Omega, I_D=7A$	---	11.5	---	nS
T_r	Turn-on Rise Time		---	29	---	
$T_{d(off)}$	Turn-off Delay Time		---	42	---	
T_f	Turn-off Fall Time		---	18	---	
Q_g	Total Gate Charge	$V_{DS}=80V, V_{GS}=10V, I_D=7A$	---	36	---	nC
Q_{gs}	Gate-Source Charge		---	5	---	
Q_{gd}	Gate-Drain Charge		---	10	---	
Source-Drain Characteristics ($T_J=25^{\circ}\text{C}$)						
V_{SD}	Diode Forward Voltage _z	$V_{GS}=0V, I_S=1A, T_J=25^{\circ}\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=7A, di/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	48	---	nS
Q_{rr}	Reverse Recovery Charge		---	29	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

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Typical Characteristics

Fig.1 Typical Output Characteristics

Fig.2 On-Resistance vs. G-S Voltage

Fig.3 Source Drain Forward Characteristics

Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ vs. T_J

Fig.6 Normalized $R_{DS(on)}$ vs. T_J

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Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Switching Waveform

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SOP-8 Package Outline Data


Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	1.35	1.55	1.75	A₁	0.10	0.18	0.25
A₂	1.25	1.45	1.65	A₃	--	0.25	--
b_p	0.36	0.42	0.51	c	0.19	0.22	0.25
D	4.70	4.92	5.10	E	3.80	3.90	4.00
e	--	1.27	--	H_E	5.80	6.00	6.20
L	--	1.05	--	L_p	0.40	0.68	1.00
Q	0.60	0.65	0.73	v	--	0.25	--
w	--	0.25	--	y	--	0.10	--
Z	0.30	0.50	0.70	θ	0°		8°