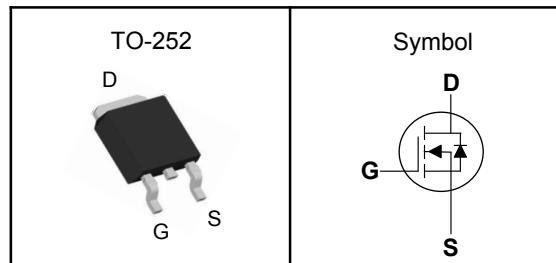


## N-Channel Enhancement Mode MOSFET

### Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

### Pin Description



### Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

$V_{DSS}$	40	V
$R_{DS(ON)-Typ}$	23	$\text{m}\Omega$
$I_D$	23	A

### Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	40	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$E_{AS}$	Single Pulse Avalanche Energy <sup>③</sup>	15.8	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	46	A
$I_D$	Continuous Drain Current	$T_c=25^\circ\text{C}$	A
$P_D$	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	W

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sub>1</sub> (Max)	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sub>1</sub>	5	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.

**N-Channel Enhancement Mode MOSFET**
**Electrical Characteristics** ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

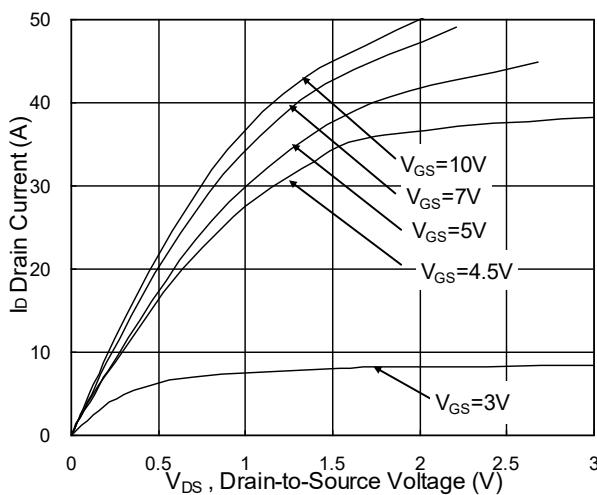
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\text{mA}$	40	---	---	V
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=32\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_{\text{D}}=250\mu\text{A}$	1.0	---	2.5	V
$\text{I}_{\text{GSS}}$	Gate Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=12\text{A}$	---	23	28	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=10\text{A}$	---	31	38	$\text{m}\Omega$
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=15\text{V}$ , Freq.=1MHz	---	593	---	pF
$C_{\text{oss}}$	Output Capacitance		---	76	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	56	---	
$T_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=20\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_{\text{G}}=3.3\Omega$ , $I_{\text{D}}=1\text{A}$	---	8.9	---	nS
$T_{\text{r}}$	Turn-on Rise Time		---	2.2	---	
$T_{\text{d(off)}}$	Turn-off Delay Time		---	41	---	
$T_{\text{f}}$	Turn-off Fall Time		---	2.7	---	
$Q_{\text{g}}$	Total Gate Charge	$V_{\text{DS}}=20\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=12\text{A}$	---	5.5	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	1.25	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	2.5	---	
<b>Source-Drain Characteristics</b> ( $T_J=25^\circ\text{C}$ )						
$V_{\text{SD}}$	Diode Forward Voltage <sub>2</sub>	$V_{\text{GS}}=0\text{V}$ , $I_{\text{S}}=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V

Note ④ : Pulse test (pulse width $\leq 300\text{us}$ , duty cycle $\leq 2\%$ ).

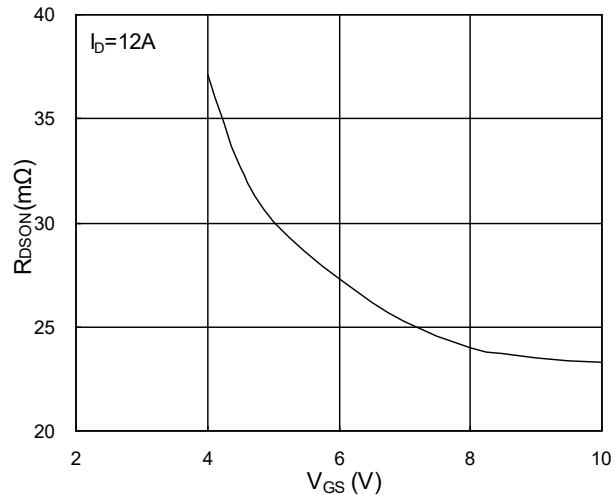
Note ⑤ : Guaranteed by design, not subject to production testing.

## N-Channel Enhancement Mode MOSFET

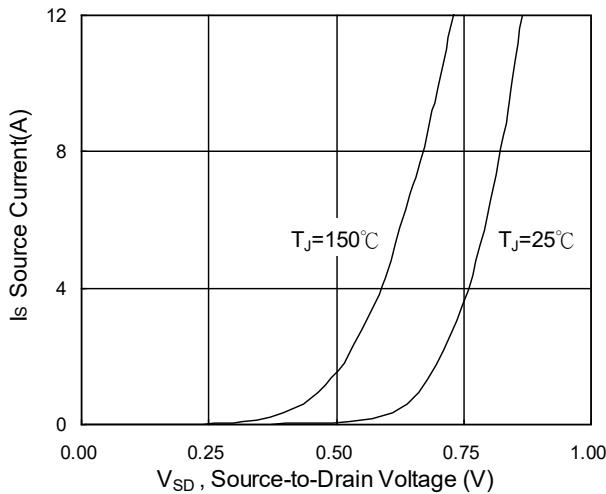
### Typical Characteristics



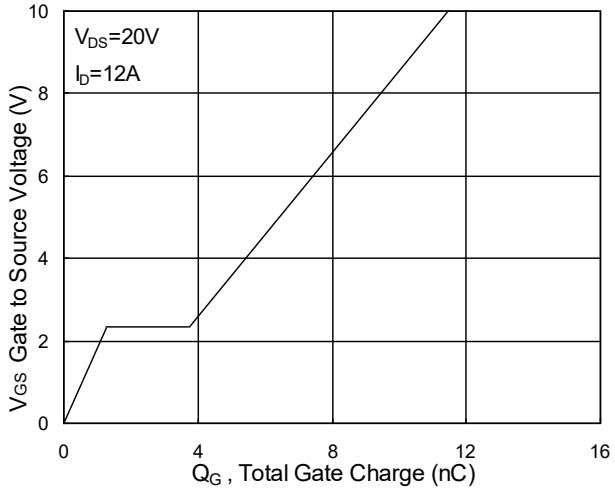
**Fig.1 Typical Output Characteristics**



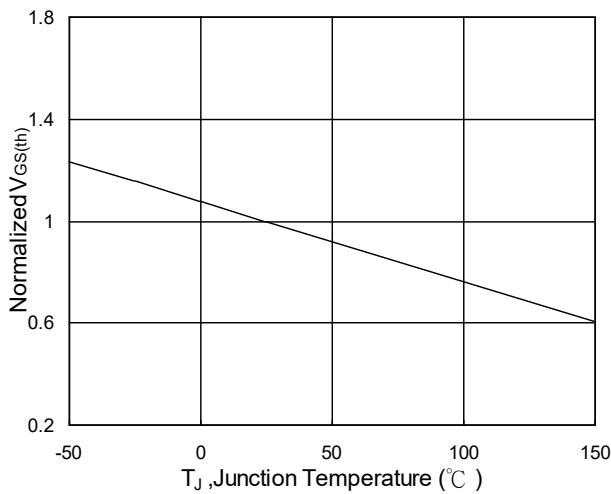
**Fig.2 On-Resistance vs. G-S Voltage**



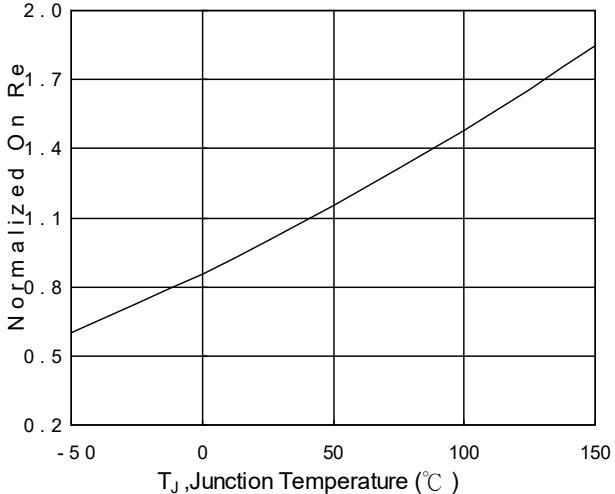
**Fig.3 Forward Characteristics of Reverse**



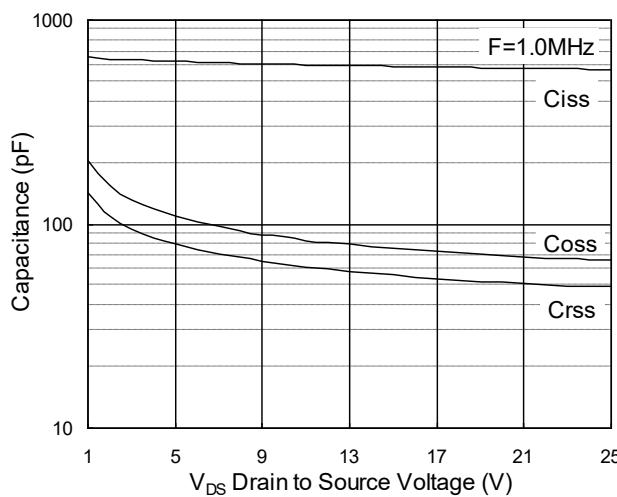
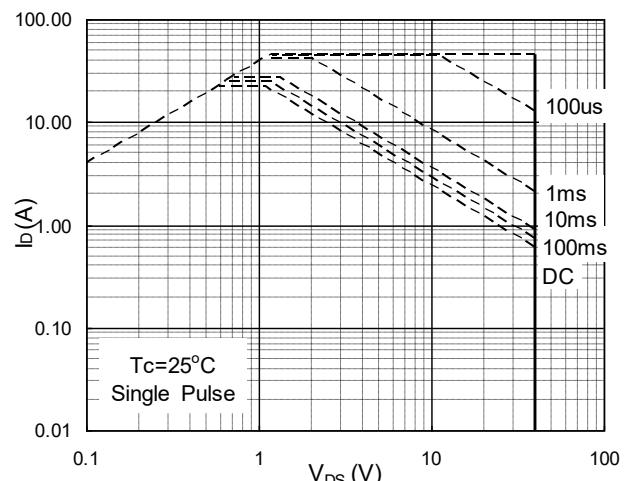
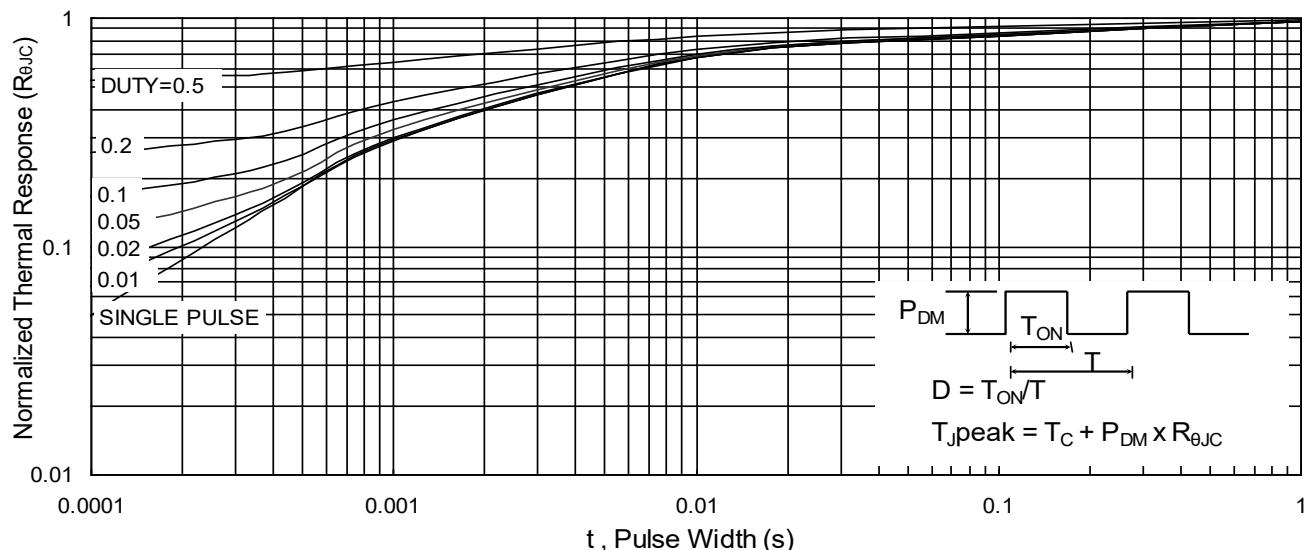
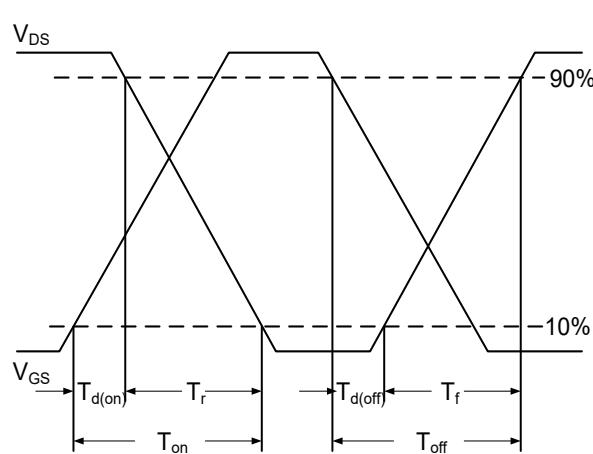
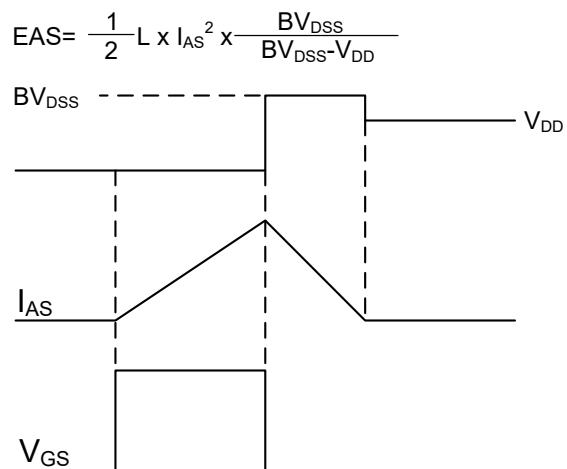
**Fig.4 Gate-Charge Characteristics**

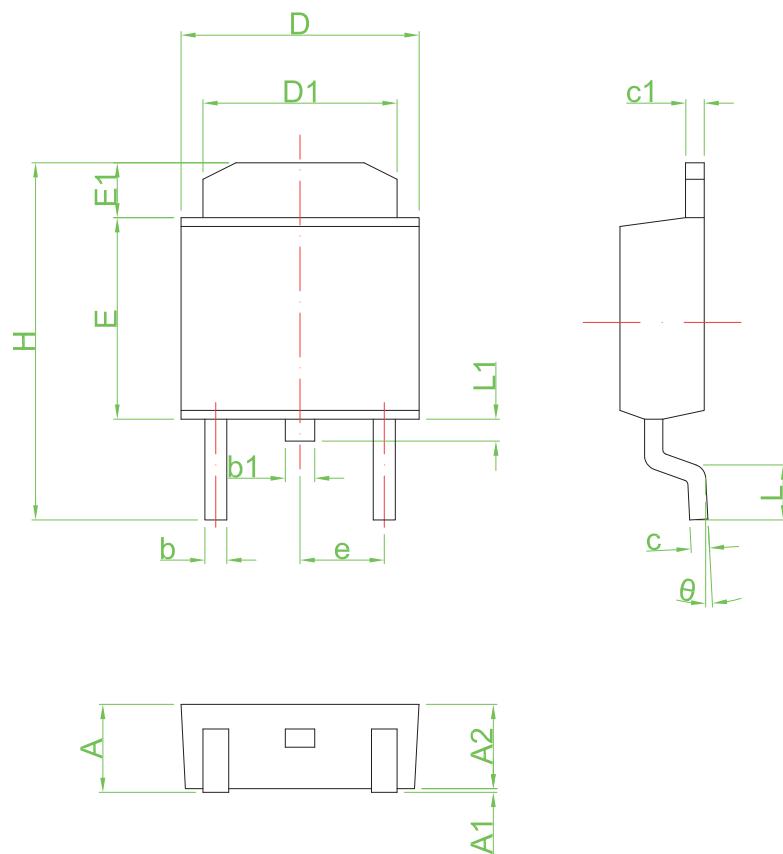


**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**

**N-Channel Enhancement Mode MOSFET**

**Fig.7 Capacitance**

**Fig.8 Safe Operating Area**

**Fig.9 Normalized Maximum Transient Thermal Impedance**

**Fig.10 Switching Time Waveform**

**Fig.11 Unclamped Inductive Switching Waveform**

**N-Channel Enhancement Mode MOSFET**
**TO-252 Package Outline Dimensions**


Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	2.25	2.65	0.089	0.104
A1	0.00	0.15	0.000	0.006
A2	2.20	2.40	0.087	0.094
b	0.50	0.70	0.020	0.028
b1	0.70	0.90	0.028	0.035
c	0.46	0.66	0.018	0.026
c1	0.46	0.66	0.018	0.026
D	6.30	6.70	0.248	0.264
D1	5.20	5.40	0.205	0.213
E	5.30	5.70	0.209	0.224
E1	1.40	1.60	0.055	0.063
H	9.40	9.90	0.370	0.390
e	2.30 TYP		0.09 TYP	
L	1.40	1.77	0.055	0.070
L1	0.50	0.70	0.020	0.028
θ	0°	8°	0°	8°