

## Dual N-Channel Enhancement Mode MOSFET

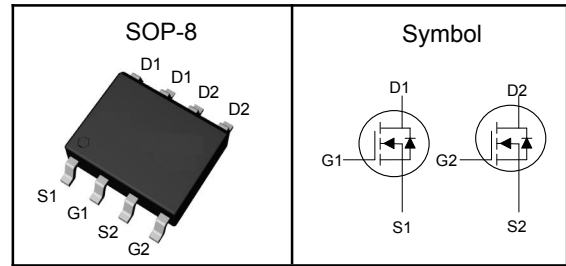
### Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

### Applications

- Power Management in Desktop Computer
- DC/DC Converters

### Pin Description



$V_{DSS}$	30	V
$R_{DS(ON)-Typ}$	16	m $\Omega$
$I_D$	6	A

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	30	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$E_{AS}$	Single Pulse Avalanche Energy <sup>③</sup>	10	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	15	A
$I_D$	Continuous Drain Current	$T_A=25^\circ\text{C}$	A
$P_D$	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	W

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sub>1</sub> (Steady State)	110	$^\circ\text{C/W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.



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Electrical Characteristics ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

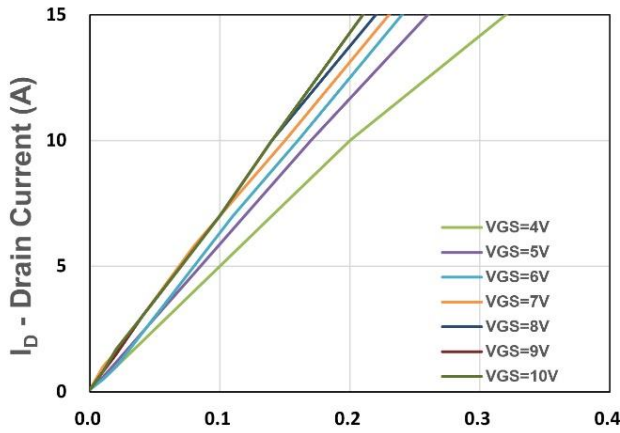
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	---	---	1	$\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	---	2.0	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=10A$	---	16	19	m $\Omega$
		$V_{GS}=4.5V, I_D=5A$	---	20	26	
$g_{fs}$	Forward Transconductance	$V_{DS}=5V, I_D=10A$	---	8	---	S
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$C_{iss}$	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, \text{Freq.}=1\text{MHz}$	---	500	---	pF
$C_{oss}$	Output Capacitance		---	62	---	
$C_{rss}$	Reverse Transfer Capacitance		---	53	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=15V, V_{GS}=10V, R_G=6\Omega, I_D=1A$	---	3.2	---	nS
$T_r$	Turn-on Rise Time		---	21.6	---	
$T_{d(off)}$	Turn-off Delay Time		---	25	---	
$T_f$	Turn-off Fall Time		---	18.2	---	
$Q_g$	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V, I_D=10A$	---	15.4	---	nC
$Q_{gs}$	Gate-Source Charge		---	1.3	---	
$Q_{gd}$	Gate-Drain Charge		---	4.8	---	
<b>Source-Drain Characteristics (<math>T_J=25^\circ\text{C}</math>)</b>						
$V_{SD}$	Diode Forward Voltage <sup>②</sup>	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	0.7	1.1	V
$t_{rr}$	Reverse Recovery Time	$I_F=1A, di/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	9	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	3	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu s$ , duty cycle $\leq 2\%$ ).

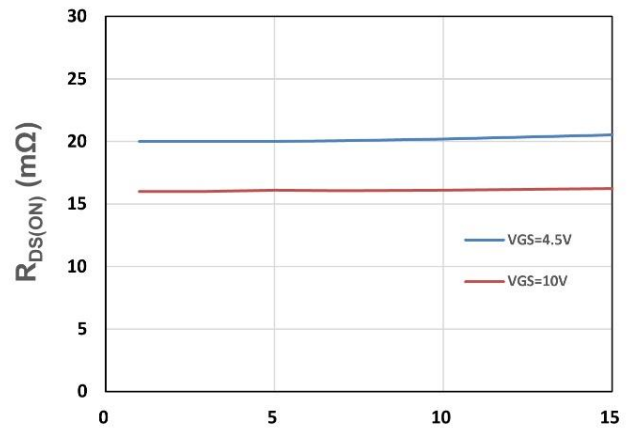
Note ⑤ : Guaranteed by design, not subject to production testing.

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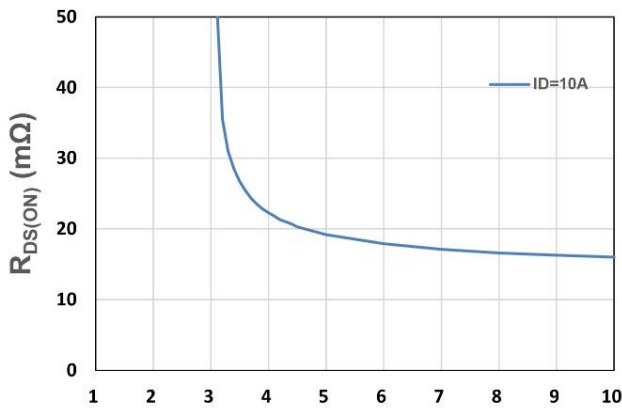
Typical Characteristics



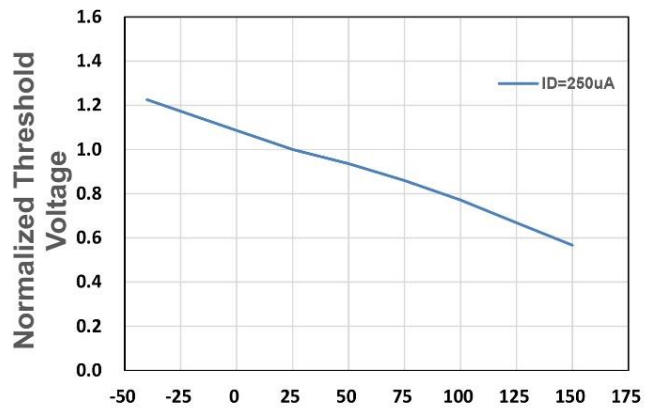
$V_{DS}$  - Drain - Source Voltage (V)  
Figure 1. Output Characteristics



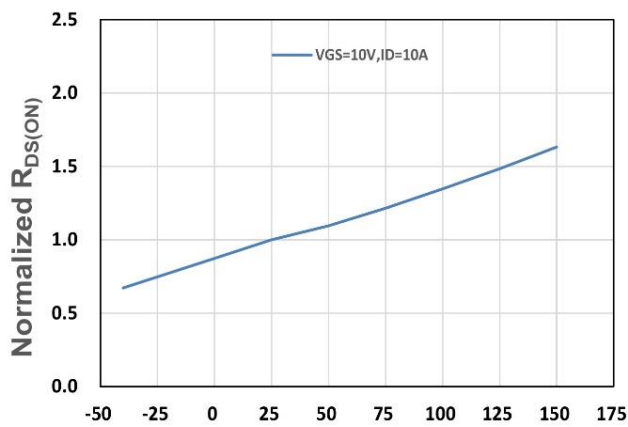
$I_D$  - Drain Current (A)  
Figure 2. On-Resistance vs.  $I_D$



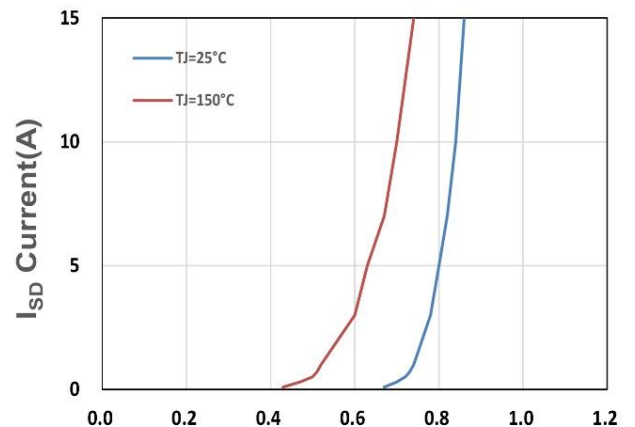
$V_{GS}$  - Gate - Source Voltage (V)  
Figure 3. On-Resistance vs.  $V_{GS}$



$T_j$ , Junction Temperature( $^{\circ}C$ )  
Figure 4. Gate Threshold Voltage

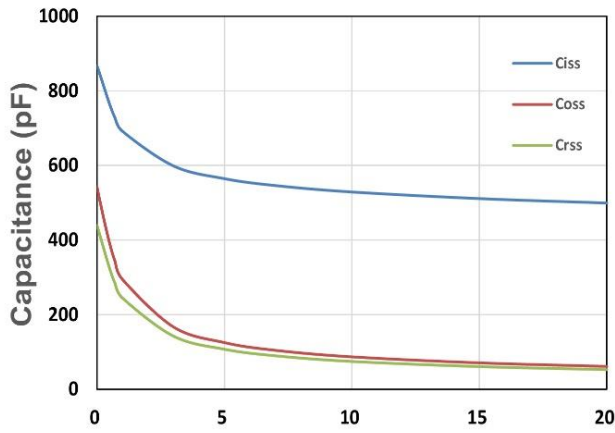


$T_j$ , Junction Temperature( $^{\circ}C$ )  
Figure 5. Drain-Source On Resistance

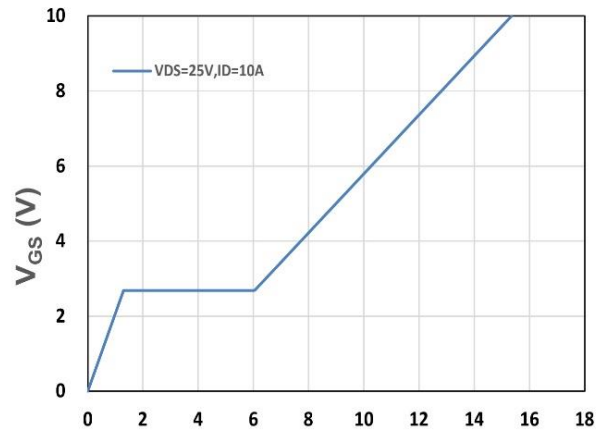


$V_{SD}$ , Source-Drain Voltage(V)  
Figure 6. Source-Drain Diode Forward

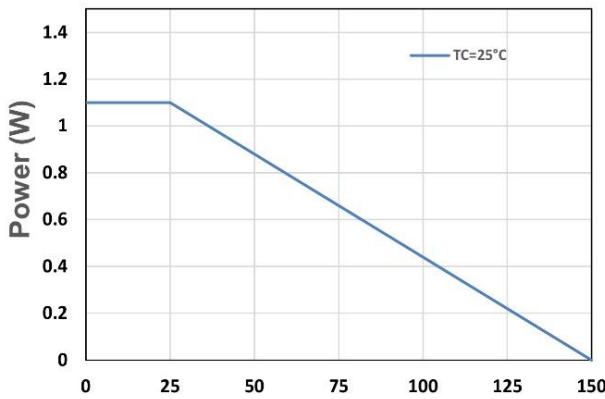
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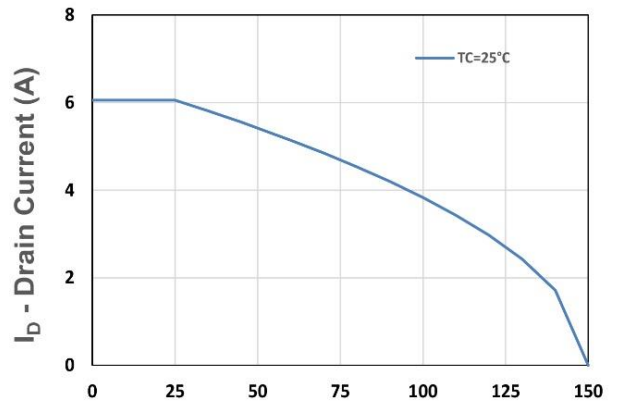
$V_{DS}$  - Drain - Source Voltage (V)  
Figure 7. Capacitance



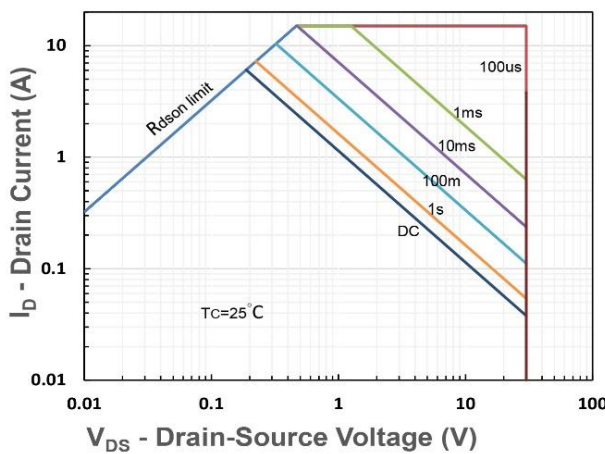
$Q_g$ , Total Gate Charge (nC)  
Figure 8. Gate Charge Characteristics



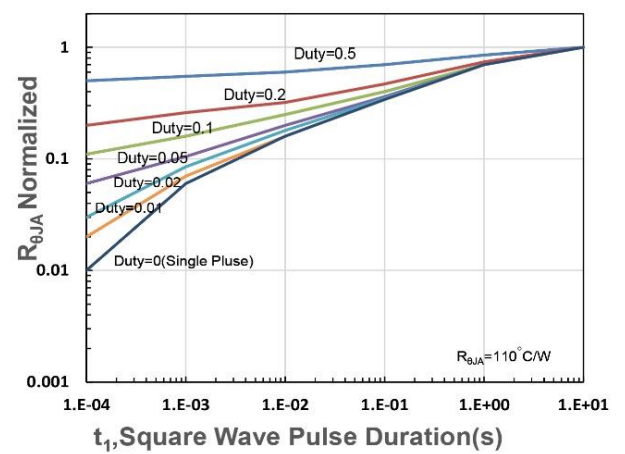
$T_j$  - Junction Temperature (°C)  
Figure 9. Power Dissipation



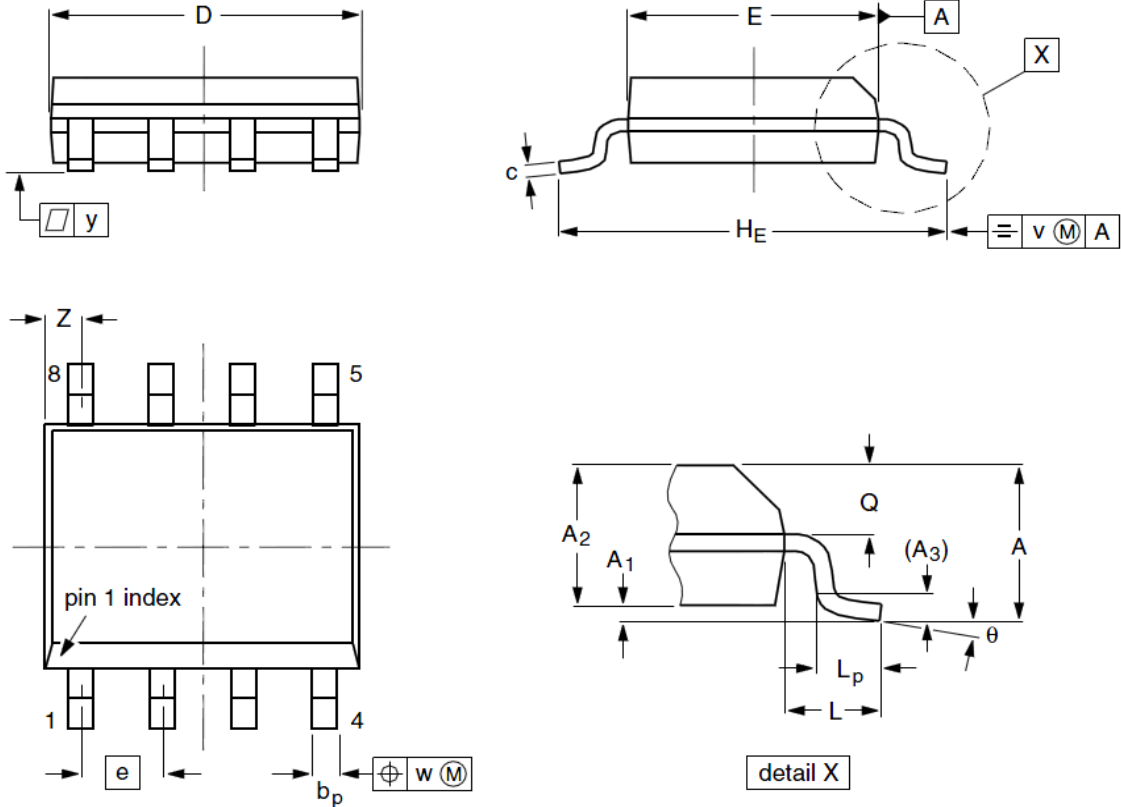
$T_j$  - Junction Temperature (°C)  
Figure 10. Drain Current



$V_{DS}$  - Drain-Source Voltage (V)  
Figure 11. Safe Operating Area



$t_1$ , Square Wave Pulse Duration(s)  
Figure 12.  $R_{\theta JA}$  Transient Thermal Impedance

**Dual N-Channel Enhancement Mode MOSFET**
**SOP-8 Package Outline Data**


Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	1.35	1.55	1.75	<b>A<sub>1</sub></b>	0.10	0.18	0.25
<b>A<sub>2</sub></b>	1.25	1.45	1.65	<b>A<sub>3</sub></b>	--	0.25	--
<b>b<sub>p</sub></b>	0.36	0.42	0.51	<b>c</b>	0.19	0.22	0.25
<b>D</b>	4.70	4.92	5.10	<b>E</b>	3.80	3.90	4.00
<b>e</b>	--	1.27	--	<b>H<sub>E</sub></b>	5.80	6.00	6.20
<b>L</b>	--	1.05	--	<b>L<sub>p</sub></b>	0.40	0.68	1.00
<b>Q</b>	0.60	0.65	0.73	<b>v</b>	--	0.25	--
<b>w</b>	--	0.25	--	<b>y</b>	--	0.10	--
<b>Z</b>	0.30	0.50	0.70	<b>θ</b>	0°		8°