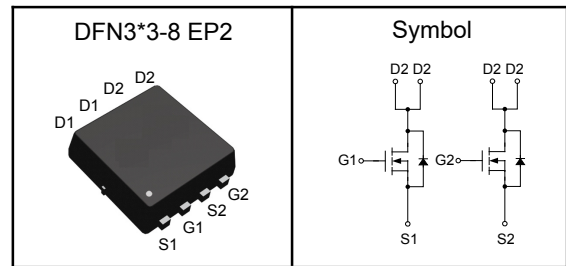


**Dual N-Channel Enhancement Mode MOSFET**
**Features**

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% Avalanche Tested

**Applications**

- Power Management in Desktop Computer
- DC/DC Converters

**Pin Description**


$V_{DSS}$	30	V
$R_{DS(ON)-Typ}$	10.2	m $\Omega$
$I_D$	8	A

**Absolute Maximum Ratings** ( $T_C=25^{\circ}C$ , Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	30	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^{\circ}C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^{\circ}C$
$I_{DM}^{①}$	Pulse Drain Current Tested	32	A
$I_D$	Continuous Drain Current	8	A
$P_D$	Maximum Power Dissipation	21	W
$E_{AS}$	Avalanche Energy, Single pulse	L=0.1mH 18	mJ

**Thermal Characteristics**

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	96	$^{\circ}C/W$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	6	$^{\circ}C/W$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^{\circ}C$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.



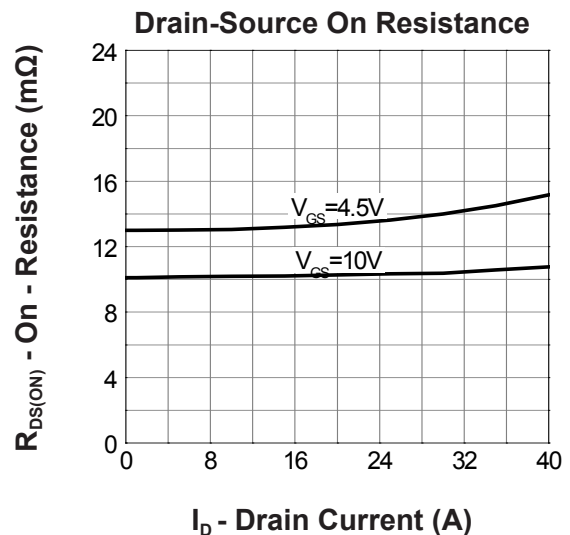
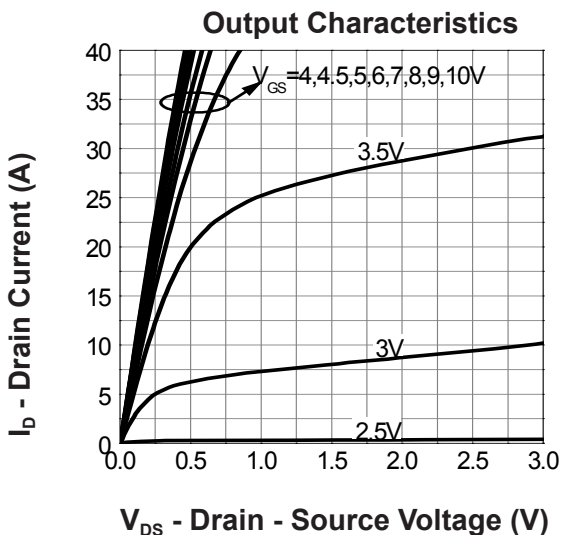
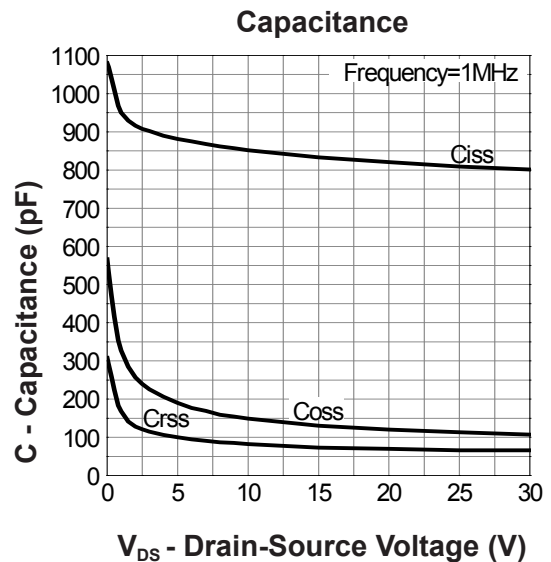
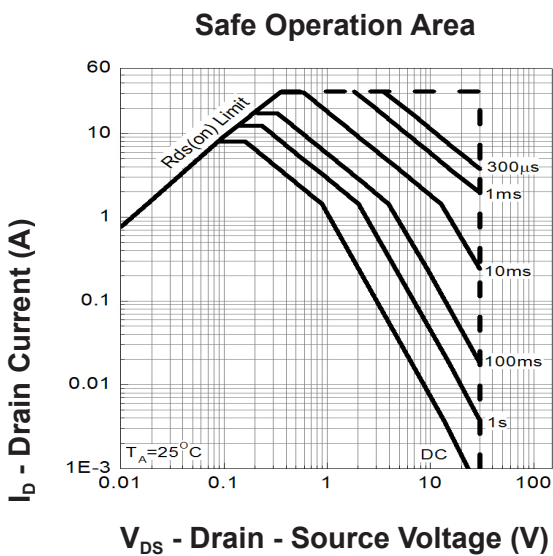
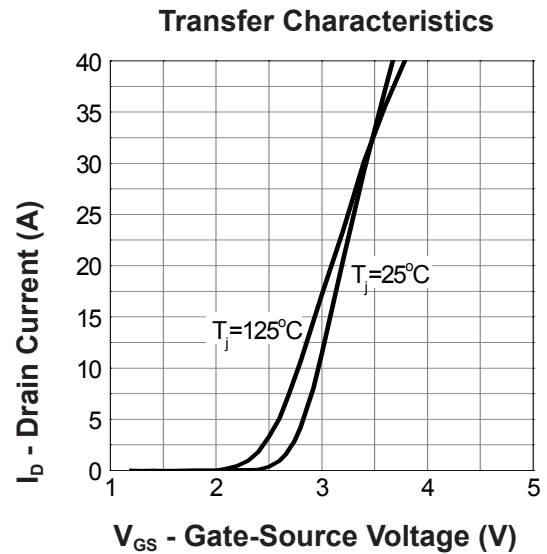
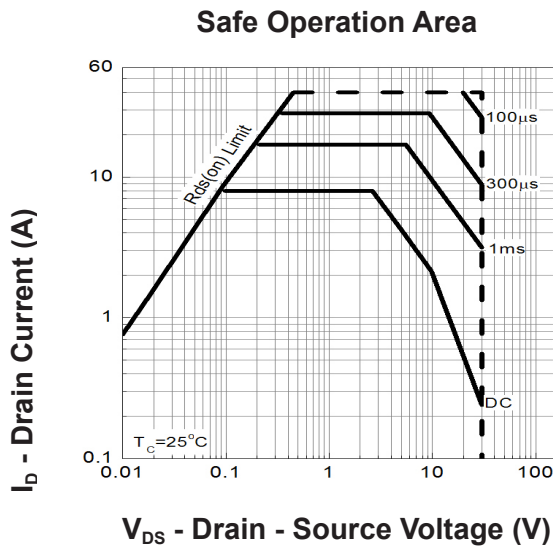
**Dual N-Channel Enhancement Mode MOSFET**

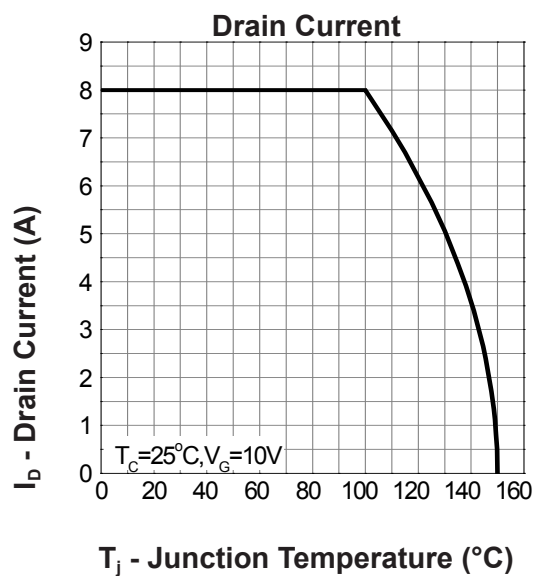
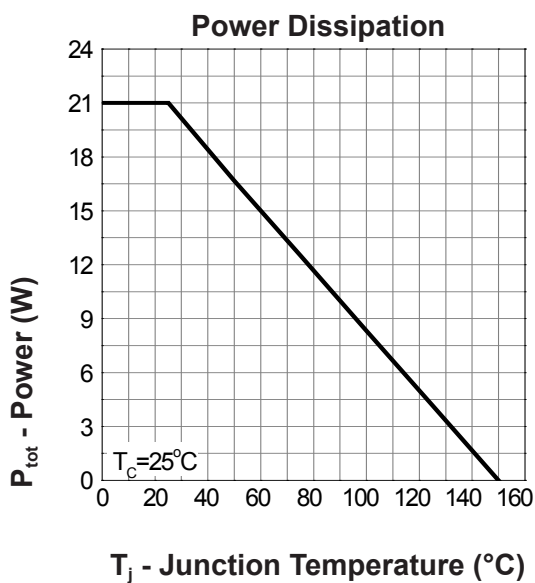
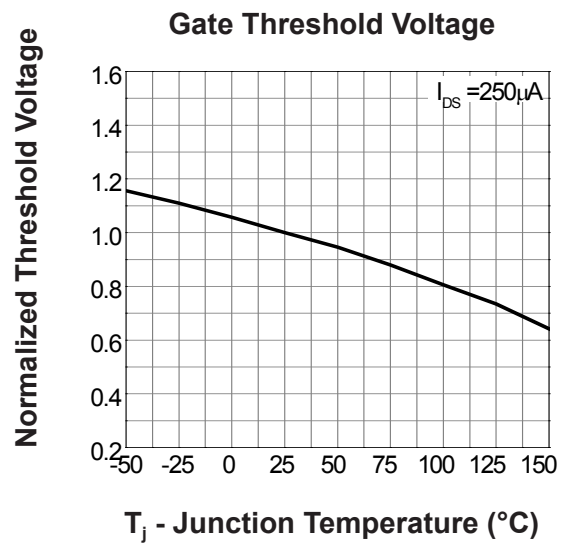
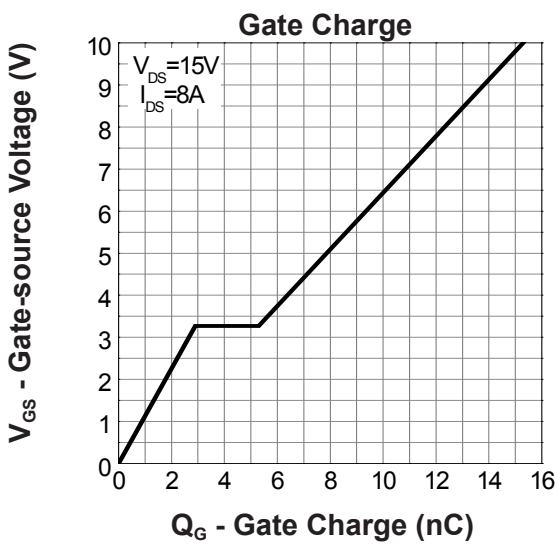
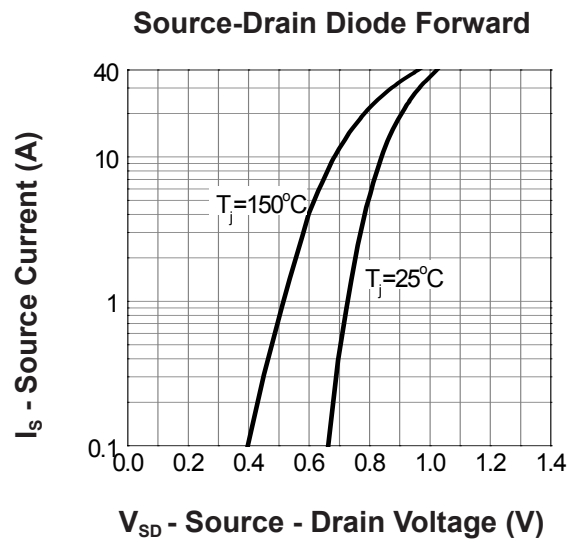
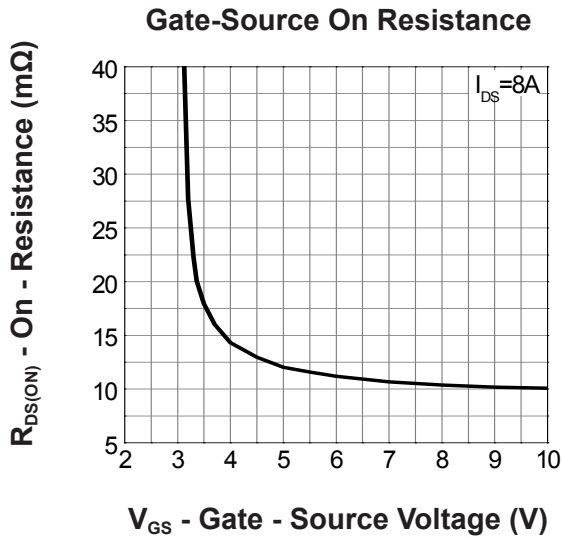
**Electrical Characteristics** ( $T_J=25^{\circ}\text{C}$ , Unless Otherwise Noted)

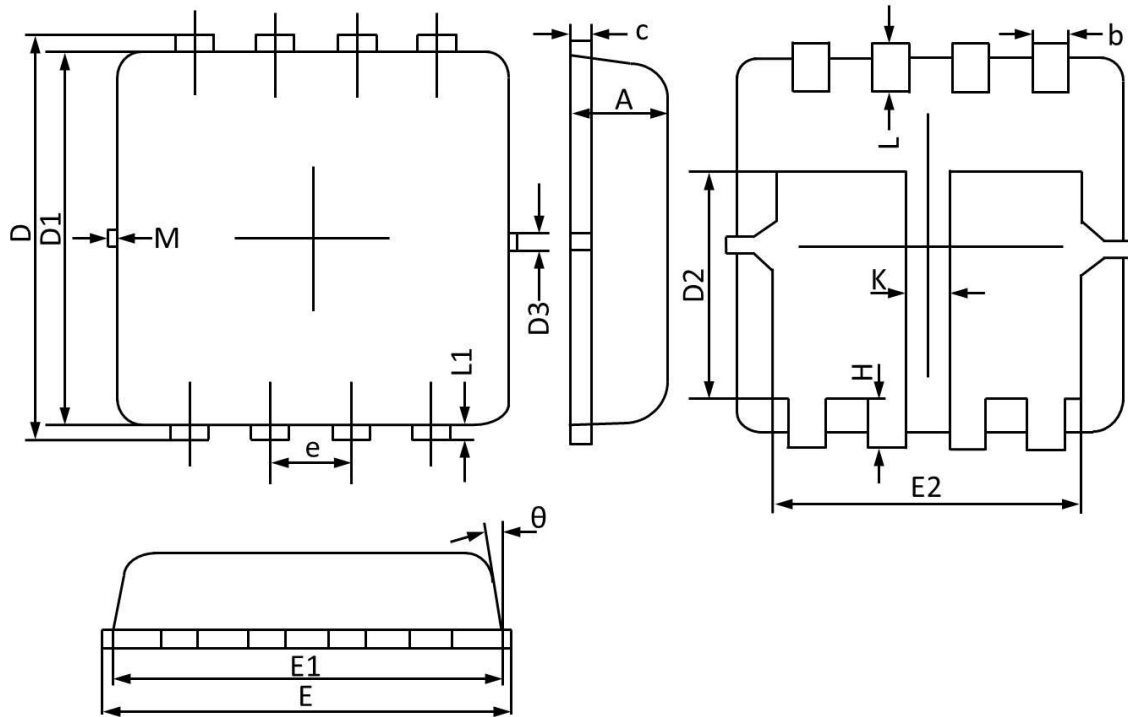
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	---	---	1	$\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.3	---	2.5	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=8A$	---	10.2	12.8	m $\Omega$
		$V_{GS}=4.5V, I_D=7A$	---	13	17.6	m $\Omega$
<b>Dynamic Characteristics</b> <sup>⑤</sup>						
$C_{iss}$	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, \text{Freq.}=1\text{MHz}$	---	833	---	pF
$C_{oss}$	Output Capacitance		---	130	---	
$C_{rss}$	Reverse Transfer Capacitance		---	73	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=15V, R_L=15\Omega, I_{DS}=1A, V_{GEN}=10V, R_G=6\Omega$	---	8	---	nS
$T_r$	Turn-on Rise Time		---	9.4	---	
$T_{d(off)}$	Turn-off Delay Time		---	23.4	---	
$T_f$	Turn-off Fall Time		---	5.2	---	
$Q_g$	Total Gate Charge	$V_{DS}=15V, V_{GS}=4.5V, I_D=8A$	---	7	---	nC
$Q_{gs}$	Gate-Source Charge		---	2.9	---	
$Q_{gd}$	Gate-Drain Charge		---	2.4	---	
<b>Source-Drain Characteristics</b>						
$V_{SD}$	Diode Forward Voltage	$I_S=8A, V_{GS}=0V$	---	0.82	1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F=8A, di/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	11.8	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	5	---	nC

Note ④: Pulse test (pulse width $\leq 300\mu s$ , duty cycle $\leq 2\%$ ).

Note ⑤: Guaranteed by design, not subject to production testing.

**Dual N-Channel Enhancement Mode MOSFET**
**Typical Characteristics**


**Dual N-Channel Enhancement Mode MOSFET**


**Dual N-Channel Enhancement Mode MOSFET**
**DFN3\*3-8 EP2 Package Outline Data**


Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	0.70	0.75	0.85	<b>E2</b>	2.35	2.50	2.60
<b>b</b>	0.25	0.30	0.35	<b>e</b>	0.65 BSC		
<b>c</b>	0.10	0.17	0.25	<b>H</b>	0.30	0.40	0.50
<b>D</b>	3.10	3.30	3.45	<b>L</b>	0.30	0.40	0.50
<b>D1</b>	2.90	3.05	3.20	<b>L1</b>	0.13 REF		
<b>D2</b>	1.45	1.70	1.95	<b>K</b>	0.30 REF		
<b>D3</b>	0.13 REF			<b>theta</b>	0°		12°
<b>E</b>	3.05	3.25	3.40	<b>M</b>	0.15 REF		
<b>E1</b>	2.90	3.10	3.25				