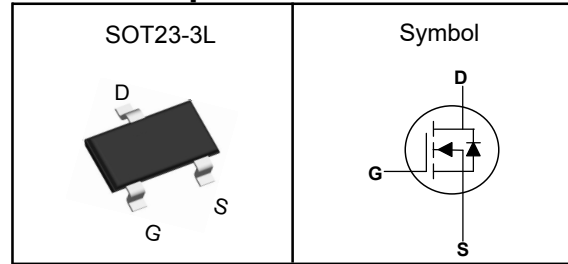


## N-Channel Enhancement Mode MOSFET

### Features

- Low R<sub>ds(on)</sub> for low conduction loss
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

### Pin Description



### Applications

- Power Management in Desktop Computer
- DC/DC Converters

V <sub>DSS</sub>	300	V
R <sub>DS(ON)-Typ</sub>	3	Ω
I <sub>D</sub>	2	A

### Absolute Maximum Ratings (T<sub>C</sub>=25°C, Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
V <sub>DSS</sub>	Drain-Source Voltage	300	V
V <sub>GSS</sub>	Gate-Source Voltage	±25	V
T <sub>J</sub>	Maximum Junction Temperature	-55 to 150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
I <sub>DM</sub> <sup>①</sup>	Pulse Drain Current Tested	12	A
I <sub>D</sub>	Continuous Drain Current	2	A
P <sub>D</sub>	Maximum Power Dissipation	35	W
E <sub>AS</sub>	Single Pulse Avalanche Energy	30	mJ

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
R <sub>θJA</sub>	Thermal Resistance-Junction to Ambient (Max)	125	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sub>1</sub>	3.5	°C/W

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C.

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.



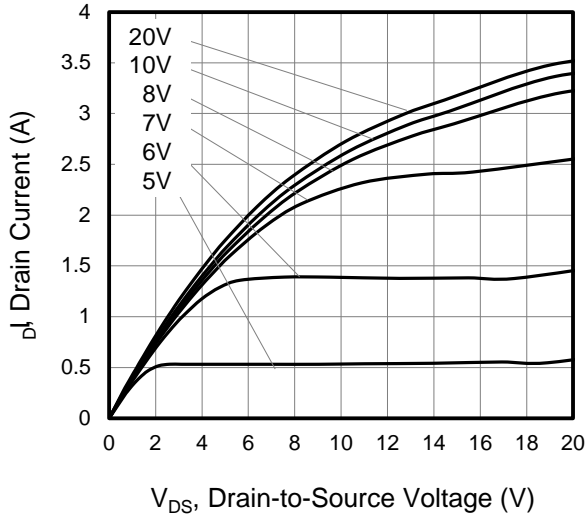
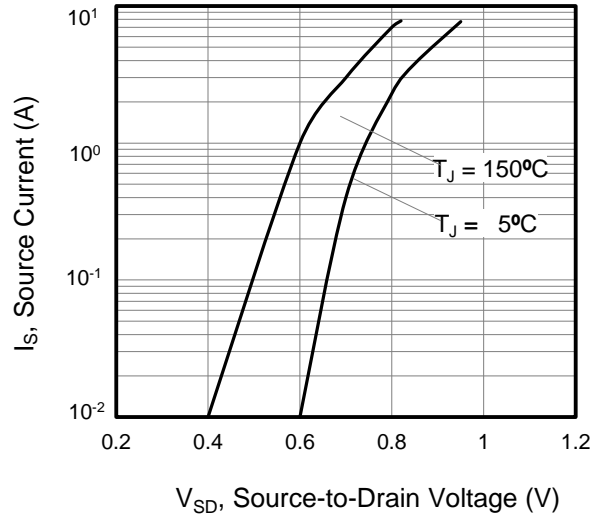
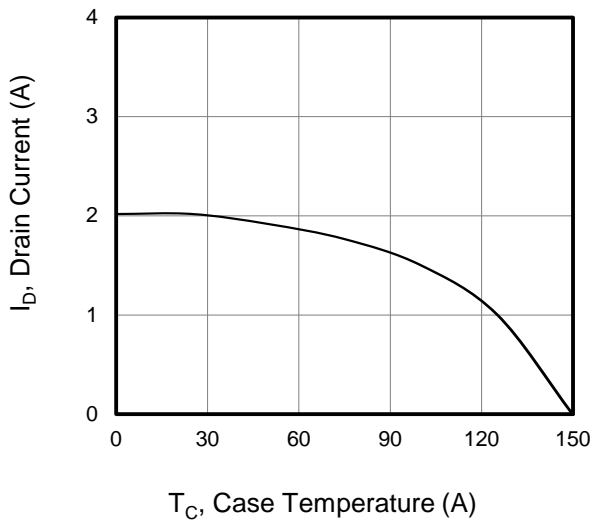
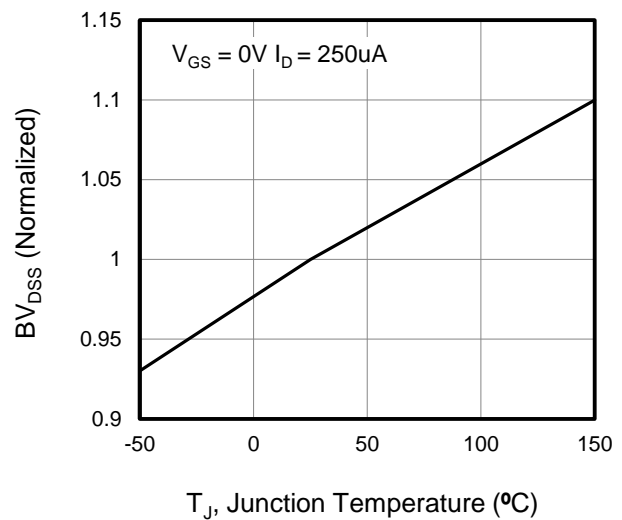
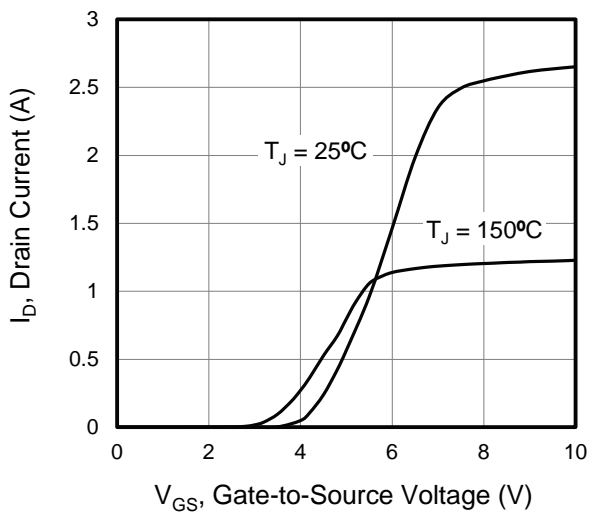
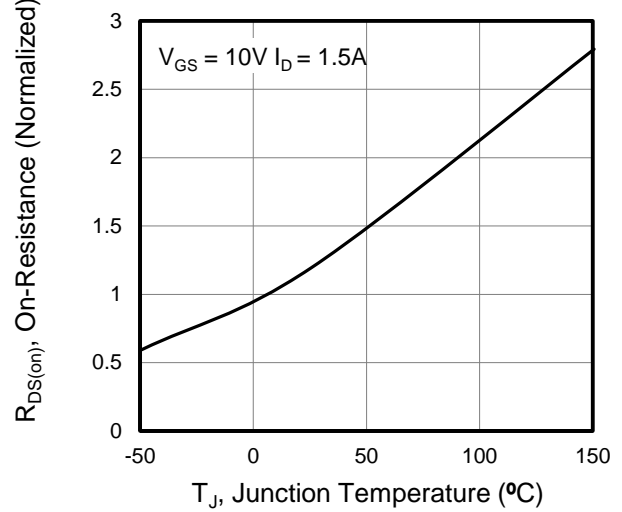
**N-Channel Enhancement Mode MOSFET**

**Electrical Characteristics** ( $T_J=25^{\circ}\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	300	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=300V, V_{GS}=0V$	---	---	1	$\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	---	4	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	---	---	$\pm 100$	nA
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=1.5A$	---	3	4	$\Omega$
<b>Dynamic Characteristics<sup>⑤</sup></b>						
$C_{iss}$	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=25V,$ Freq.=1MHz	---	138	---	pF
$C_{oss}$	Output Capacitance		---	30	---	
$C_{rss}$	Reverse Transfer Capacitance		---	5	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=150V, R_G=25\Omega,$ $I_D=3A$	---	18	---	nS
$T_r$	Turn-on Rise Time		---	55	---	
$T_{d(off)}$	Turn-off Delay Time		---	60	---	
$T_f$	Turn-off Fall Time		---	55	---	
$Q_g$	Total Gate Charge	$V_{DD}=240V, V_{GS}=10V,$ $I_D=3A$	---	4.5	---	nC
$Q_{gs}$	Gate-Source Charge		---	0.7	---	
$Q_{gd}$	Gate-Drain Charge		---	2	---	
<b>Source-Drain Characteristics (<math>T_J=25^{\circ}\text{C}</math>)</b>						
$V_{SD}^{④}$	Diode Forward Voltage	$V_{GS}=0V, I_S=3A, T_J=25^{\circ}\text{C}$	---	---	1.4	V
$t_{rr}$	Reverse Recovery Time	$I_S=3A, V_{GS}=0V$ $di/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	250	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	1.8	---	nC

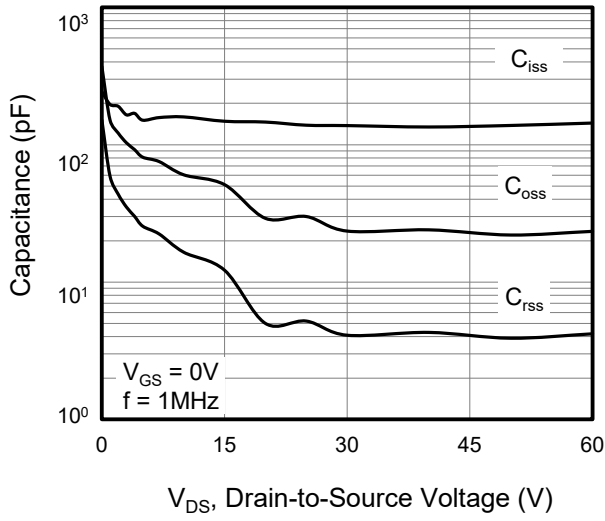
Note ④: Pulse test (pulse width 300us, duty cycle 2%).

Note ⑤: Guaranteed by design, not subject to production testing.

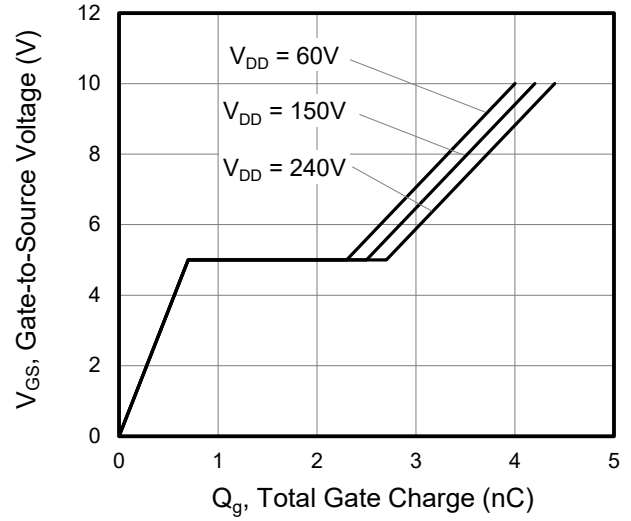
**N-Channel Enhancement Mode MOSFET**
**Typical Characteristics**
**Figure 1. Output Characteristics**

**Figure 2. Body Diode Forward Voltage**

**Figure 3. Drain Current vs. Temperature**

**Figure 4.  $BV_{DSS}$  Variation vs. Temperature**

**Figure 5. Transfer Characteristics**

**Figure 6. On-Resistance vs. Temperature**


**N-Channel Enhancement Mode MOSFET**

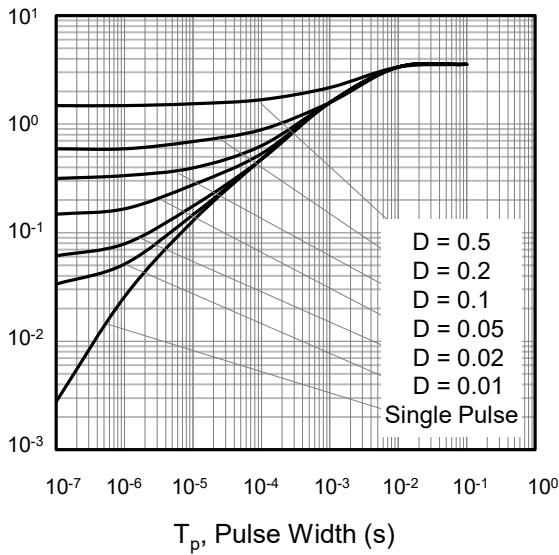
**Figure 7. Capacitance**

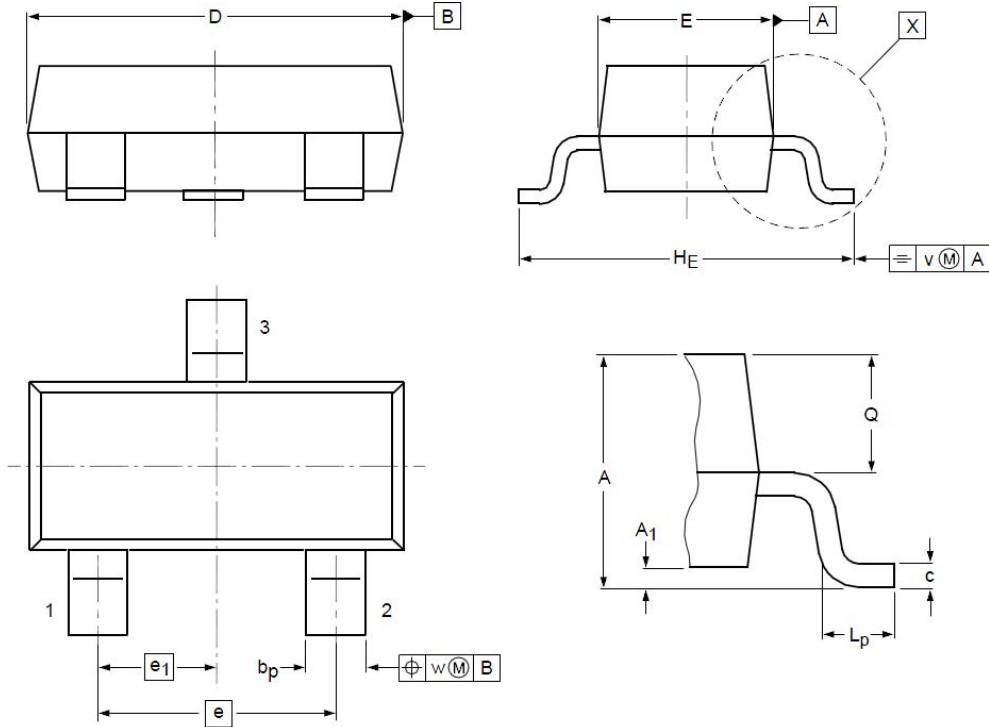


**Figure 8. Gate Charge**



**Figure 9. Transient Thermal Impedance**



**N-Channel Enhancement Mode MOSFET**
**SOT23-3L Package Outline Dimensions**


Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	0.90	1.07	1.25	<b>e<sub>1</sub></b>	--	0.95	--
<b>A<sub>1</sub></b>	0.01	0.05	0.10	<b>H<sub>E</sub></b>	2.50	2.80	3.00
<b>b<sub>p</sub></b>	0.30	0.40	0.50	<b>L<sub>P</sub></b>	0.30	0.45	0.60
<b>c</b>	0.10	0.15	0.20	<b>Q</b>	0.23	0.28	0.33
<b>D</b>	2.70	2.90	3.10	<b>V</b>	--	0.20	--
<b>E</b>	1.40	1.55	1.75	<b>W</b>	--	0.20	--
<b>e</b>	--	1.90	--				