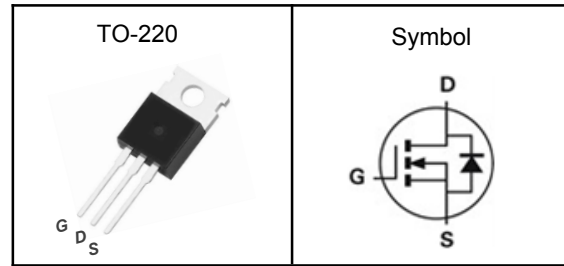


N-Channel Enhancement Mode MOSFET
Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description


V_{DSS}	200	V
$R_{DS(ON)-Typ}$	250	m Ω
I_D	9	A

Absolute Maximum Ratings($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit	
V_{DSS}	Drain-Source Voltage	200	V	
V_{GSS}	Gate-Source Voltage	± 20	V	
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
E_{AS}	Single Pulse Avalanche Energy ^③	320	mJ	
$I_{DM}^{①}$	Pulse Drain Current Tested	36	A	
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	9	A
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	74	W

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	60	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ^①	1.7	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^\circ\text{C}$.

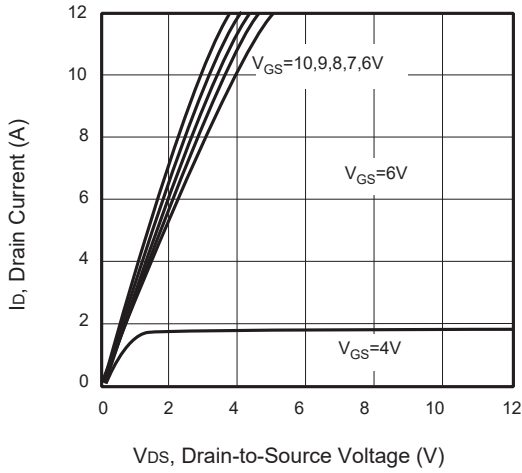
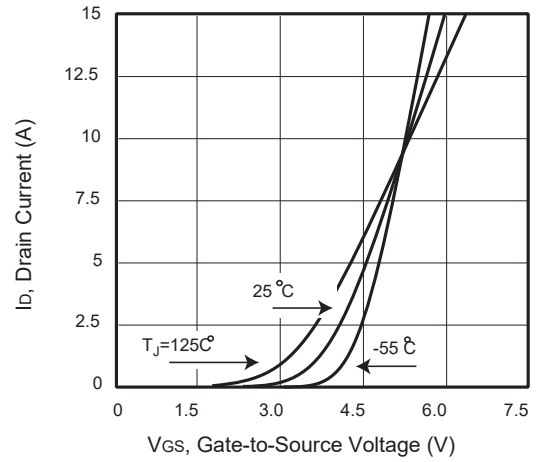
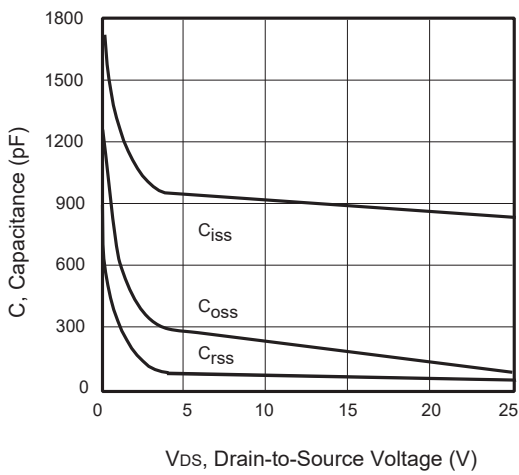
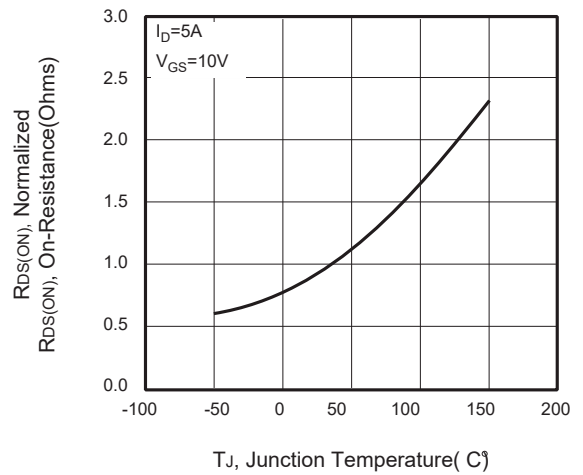
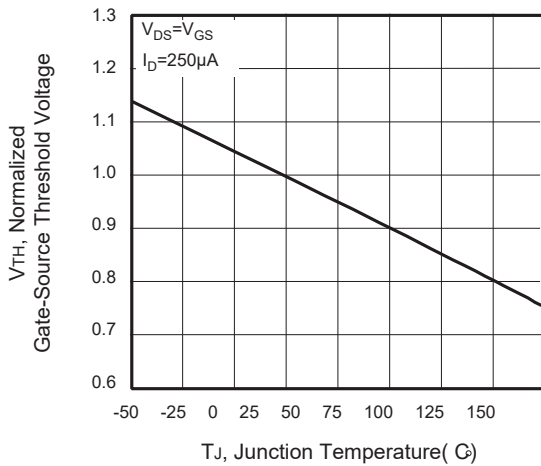
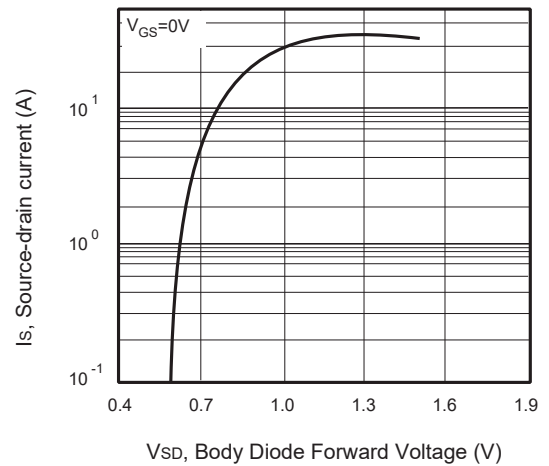
Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.

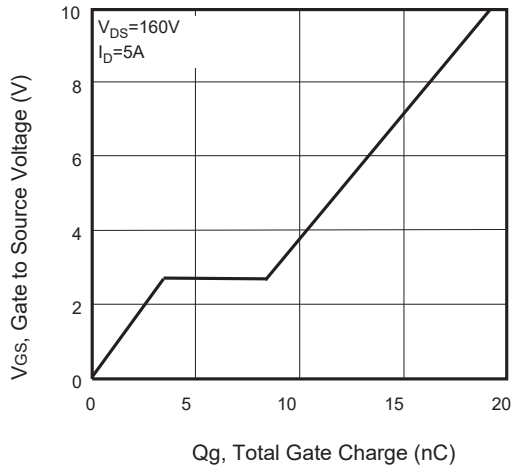
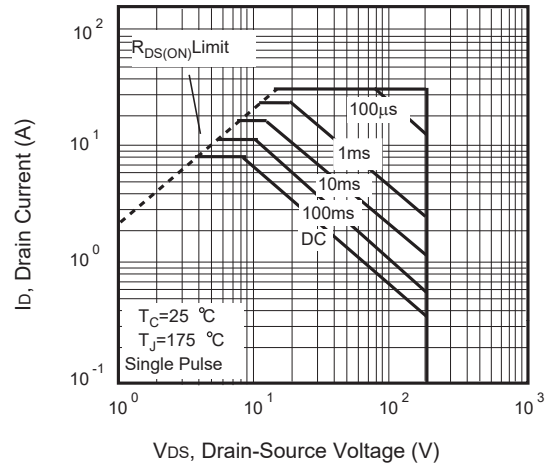
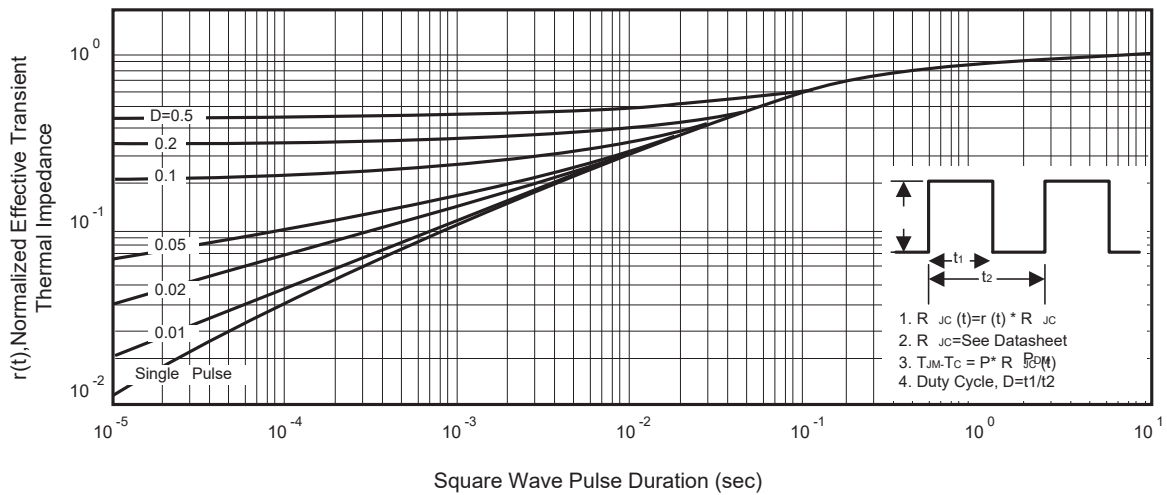
**N-Channel Enhancement Mode MOSFET****Electrical Characteristics** ($T_J=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	200	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=200V, V_{GS}=0V$	---	---	1	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	---	4	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
$R_{DS(ON)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=4.5A$	---	250	300	m Ω
Dynamic Characteristics ^⑤						
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=25V,$ Freq.=1MHz	---	850	---	pF
C_{oss}	Output Capacitance		---	105	---	
C_{rss}	Reverse Transfer Capacitance		---	25	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=100V, I_D=5A,$ $V_{GS}=10V, R_G=50\Omega$	---	24	---	nS
T_r	Turn-on Rise Time		---	15	---	
$T_{d(off)}$	Turn-off Delay Time		---	115	---	
T_f	Turn-off Fall Time		---	25	---	
Q_g	Total Gate Charge	$V_{DS}=160V, V_{GS}=10V,$ $I_D=5A$	---	19	---	nC
Q_{gs}	Gate-Source Charge		---	3	---	
Q_{gd}	Gate-Drain Charge		---	5	---	
Source-Drain Characteristics ($T_J=25^\circ\text{C}$)						
V_{SD}	Diode Forward Voltage _z	$V_{GS}=0V, I_S=9A, T_J=25^\circ\text{C}$	---	---	1.4	V
t_{rr}	Reverse Recovery Time	$I_F=9A, V_{GS}=0V$ $di/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	190	---	nS
Q_{rr}	Reverse Recovery Charge		---	1.7	---	nC

Note ④ : Pulse test (pulse width \leq 300 μ s, duty cycle \leq 2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

N-Channel Enhancement Mode MOSFET
Typical Characteristics

Figure1. Output Characteristics

Figure 2. Transfer Characteristics

Figure 3. Capacitance

Figure 4. On-Resistance Variation with Temperature

Figure 5. Gate Threshold Variation with Temperature

Figure 6. Body Diode Forward Voltage Variation with Source Current

N-Channel Enhancement Mode MOSFET

Figure 7. Gate Charge

Figure 8. Maximum Safe Operating Area

Figure 9. Normalized Thermal Transient Impedance Curve

N-Channel Enhancement Mode MOSFET
TO-220 Package Outline Data


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.350	4.650
A1	2.250	2.550
b	0.710	0.910
b1	1.170	1.400
c	0.330	0.650
c1	1.200	1.400
D	9.910	10.250
E	8.9500	9.750
E1	12.650	12.950
e	2.540 TYP.	
e1	4.980	5.180
F	2.650	2.950
H	7.900	8.100
h	0.000	0.300
L	12.700	13.500
L1	2.850	3.250
V	7.500 REF.	
Φ	3.400	3.800