

## N-Channel Enhancement Mode MOSFET

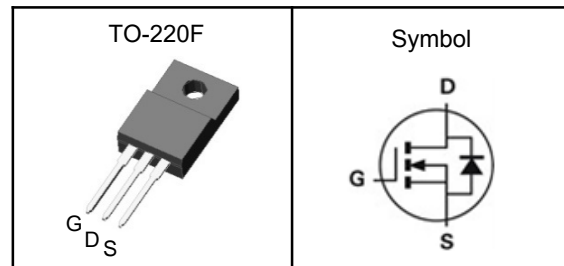
### Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

### Applications

- Power Management in Desktop Computer
- DC/DC Converters

### Pin Description



$V_{DSS}$	150	V
$R_{DS(ON)-Typ}$	11	m $\Omega$
$I_D$	80	A

### Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	150	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$E_{AS}$	Single Pulse Avalanche Energy <sup>3</sup>	392	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	320	A
$I_D$	Continuous Drain Current	$T_C=25^\circ\text{C}$ 80	A
$P_D$	Maximum Power Dissipation	$T_C=25^\circ\text{C}$ 140	W

### Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	0.87	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^\circ\text{C}$ .

Note ③ : Surface Mounted on 1in<sup>2</sup> FR-4 board with 1oz.



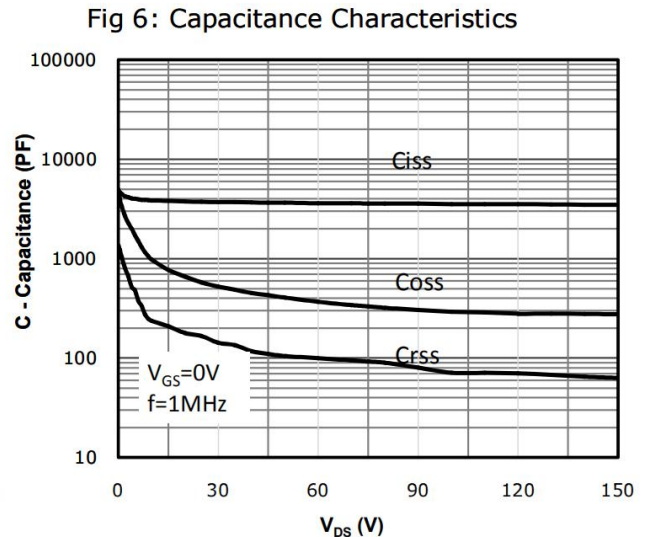
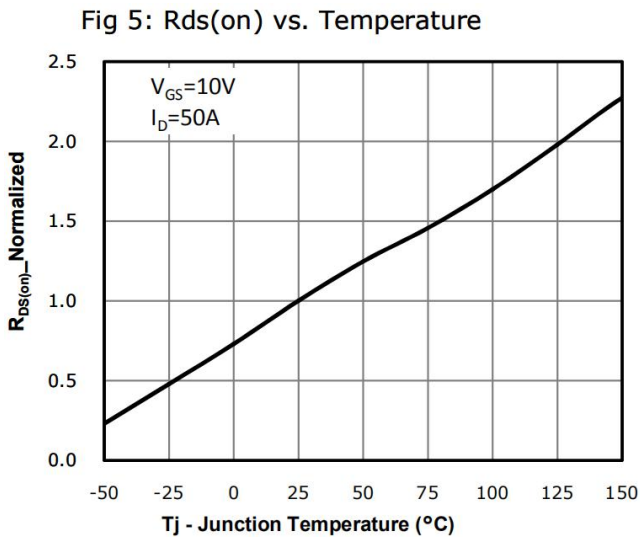
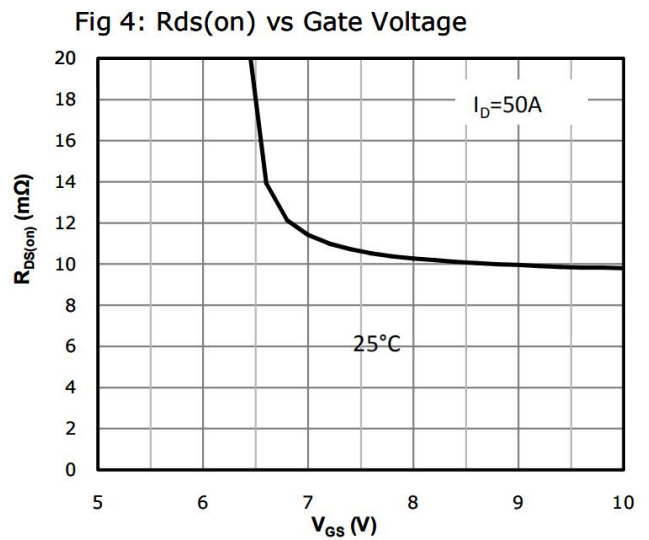
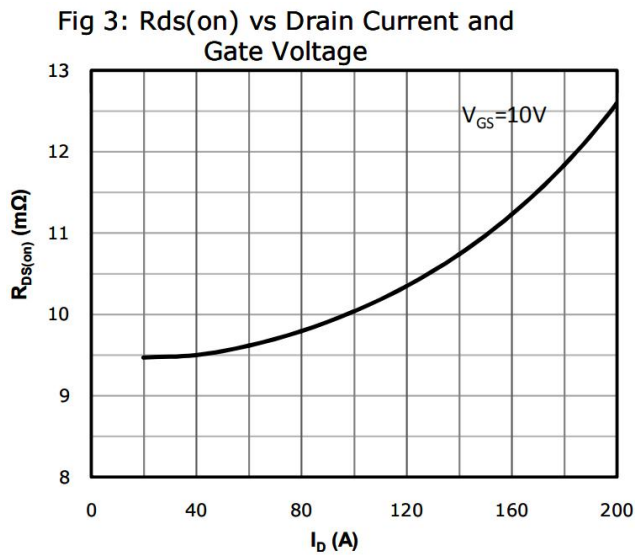
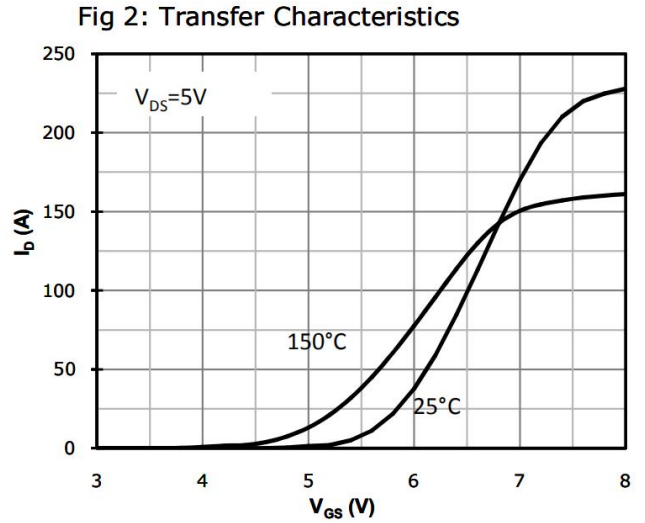
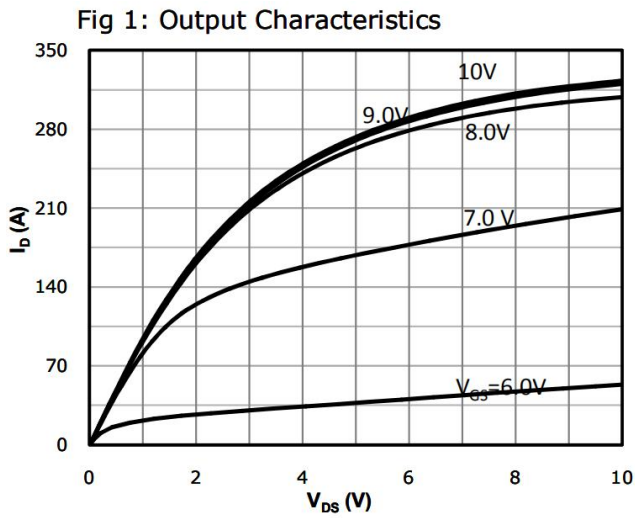
**N-Channel Enhancement Mode MOSFET**

**Electrical Characteristics** ( $T_J=25^{\circ}\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	150	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=150V, V_{GS}=0V$	---	---	1	$\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2.5	---	4.5	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$R_{DS(ON)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=30A$	---	11	13	m $\Omega$
<b>Dynamic Characteristics</b> <sup>⑤</sup>						
$C_{iss}$	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=75V,$ Freq.=1MHz	---	3420	---	pF
$C_{oss}$	Output Capacitance		---	300	---	
$C_{rss}$	Reverse Transfer Capacitance		---	85	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=75V, V_{GS}=10V,$ $R_G=3\Omega, I_D=30A$	---	18	---	nS
$T_r$	Turn-on Rise Time		---	39	---	
$T_{d(off)}$	Turn-off Delay Time		---	34	---	
$T_f$	Turn-off Fall Time		---	70	---	
$Q_g$	Total Gate Charge	$V_{DS}=75V, V_{GS}=10V,$ $I_D=30A$	---	68	---	nC
$Q_{gs}$	Gate-Source Charge		---	23	---	
$Q_{gd}$	Gate-Drain Charge		---	24	---	
<b>Source-Drain Characteristics</b> ( $T_J=25^{\circ}\text{C}$ )						
$V_{SD}$	Diode Forward Voltage <sub>2</sub>	$V_{GS}=0V, I_S=30A, T_J=25^{\circ}\text{C}$	---	---	1.3	V
$t_{rr}$	Reverse Recovery Time	$I_F=30A,$ $di/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	70	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	230	---	nC

Note ④ : Pulse test (pulse width $\leq$ 300us, duty cycles $\leq$ 2%).

Note ⑤ : Guaranteed by design, not subject to production testing.

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**Typical Characteristics**


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Fig 7: Gate Charge Characteristics

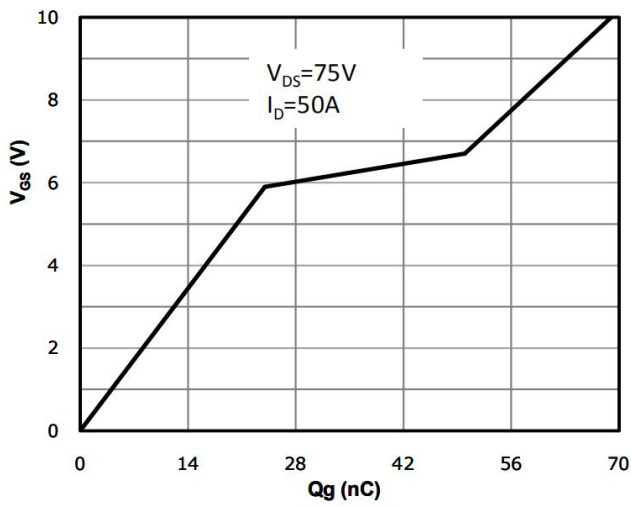


Fig 8: Body-diode Forward Characteristics

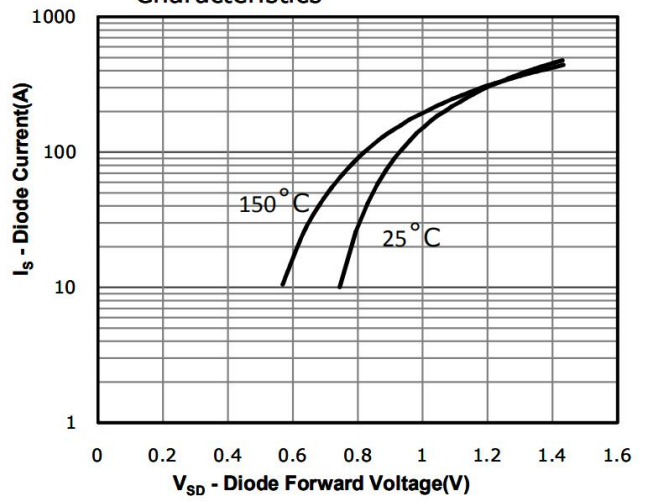
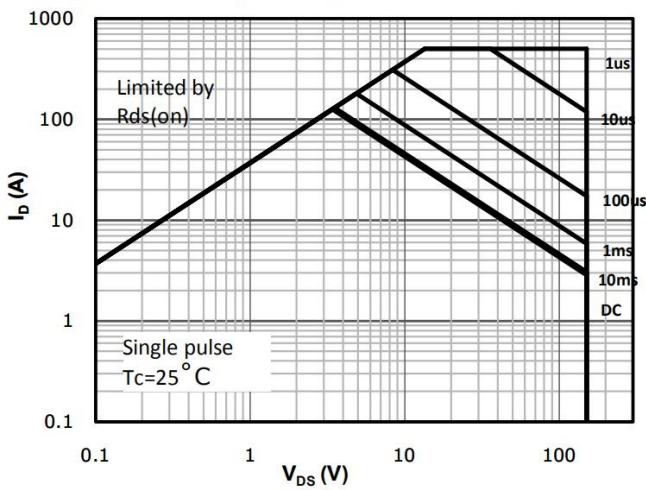


Fig 9 : Safe Operating Area



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**TO-220F Package Outline Data**
