

N-Channel Enhancement Mode MOSFET

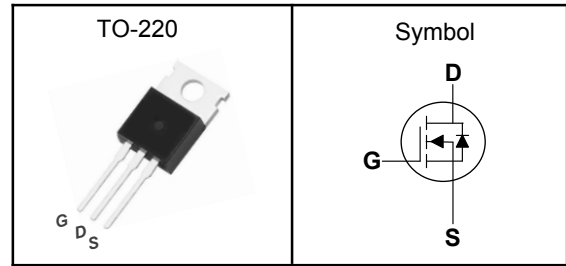
Features

- High Speed Power Switching
- Reliable and Rugged
- ROHS Compliant
- 100% Avalanche Tested

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description



V_{DSS}	85	V
$R_{DS(ON)-Typ}$	2.4	m Ω
I_D	200	A

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
V_{DSS}	Drain-Source Voltage	85	V
V_{GSS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{①}$	Pulse Drain Current Tested	640	A
I_D	Continuous Drain Current	200	A
P_D	Maximum Power Dissipation	225	W
E_{AS}	Avalanche Energy, Single pulse	900	mJ

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62.5	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.55	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C .

Note ③ : Surface Mounted on 1in^2 FR-4 board with 1oz.



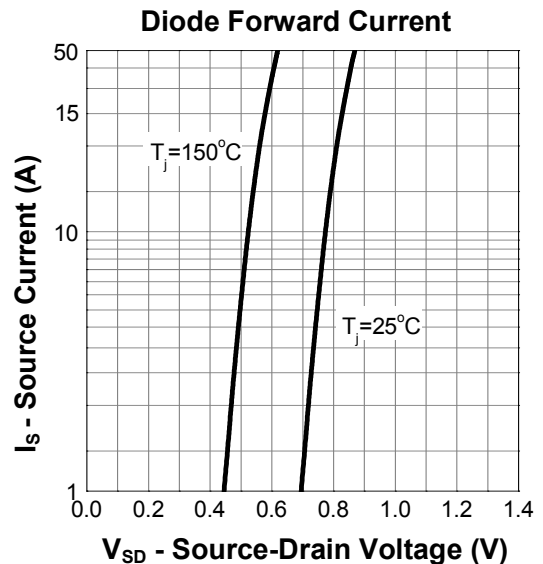
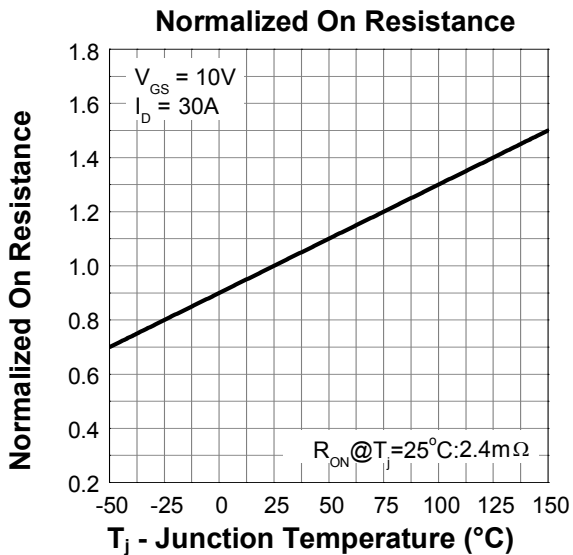
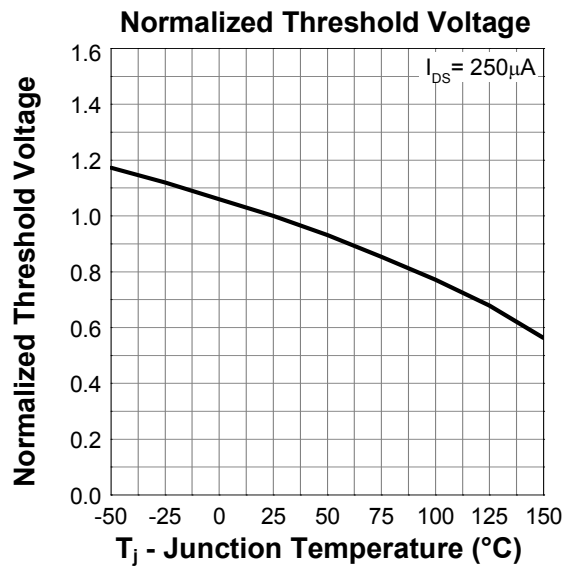
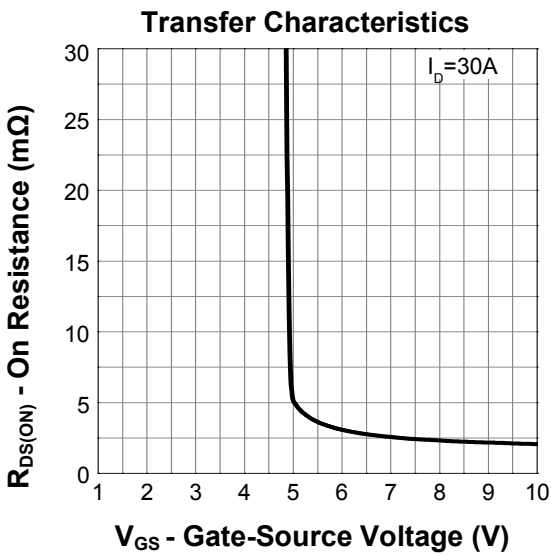
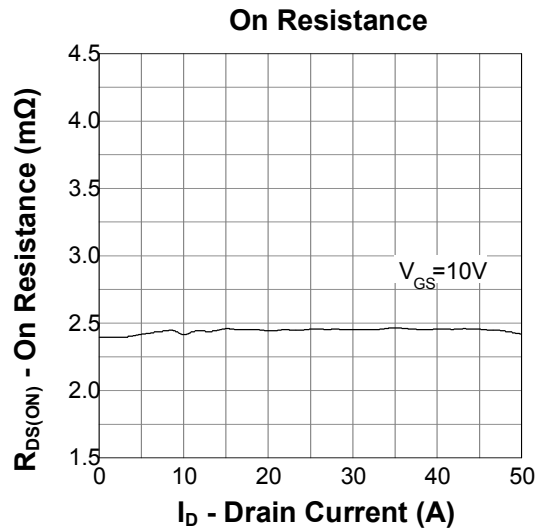
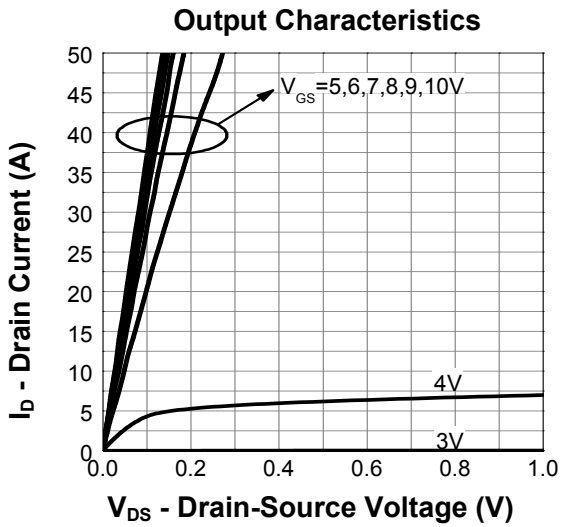
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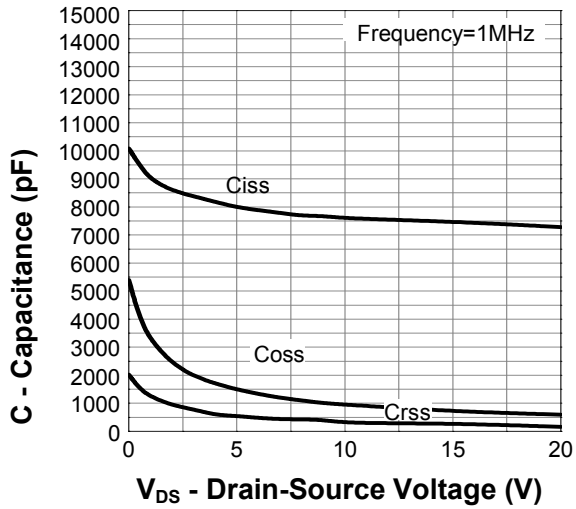
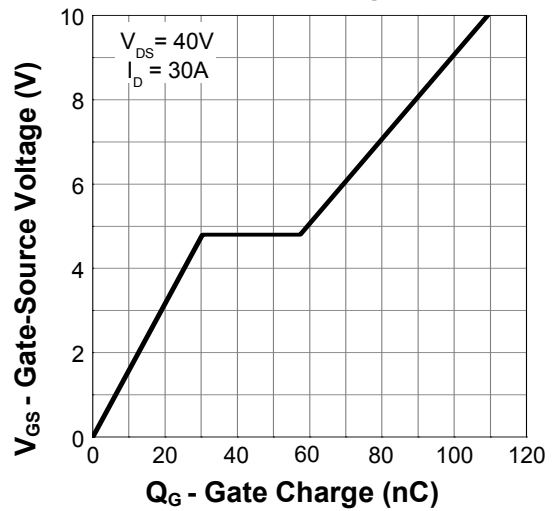
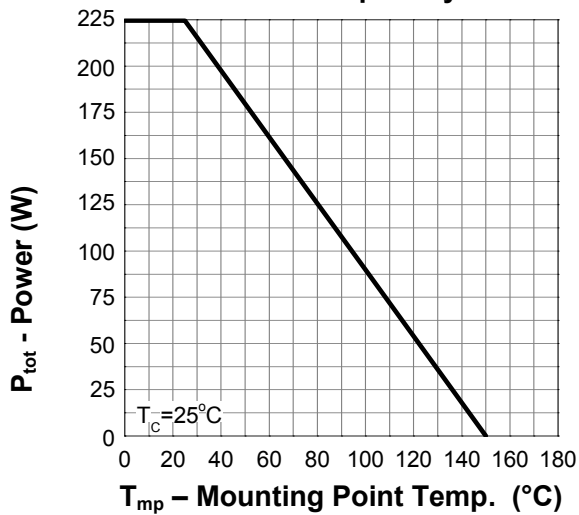
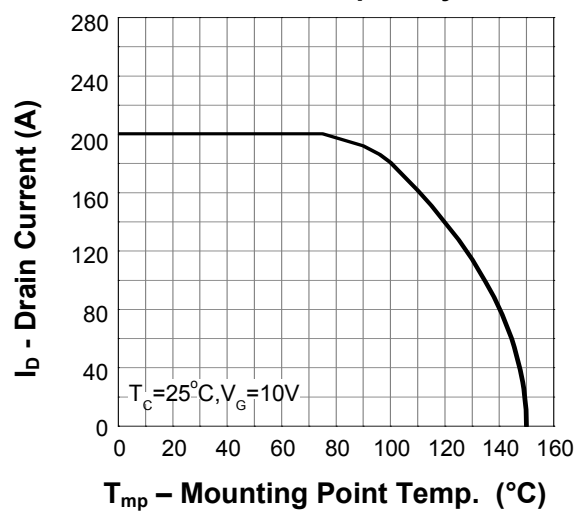
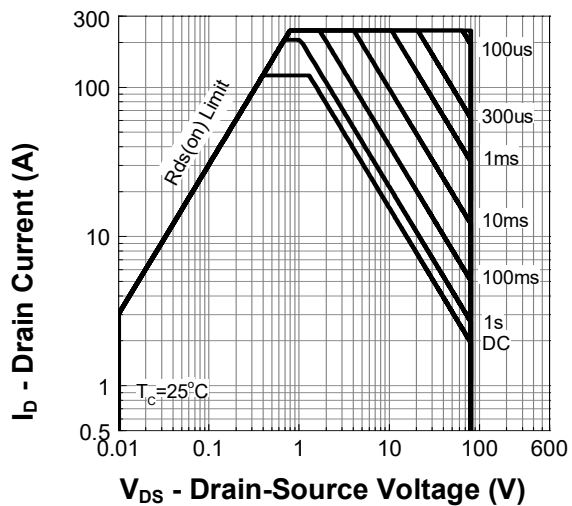
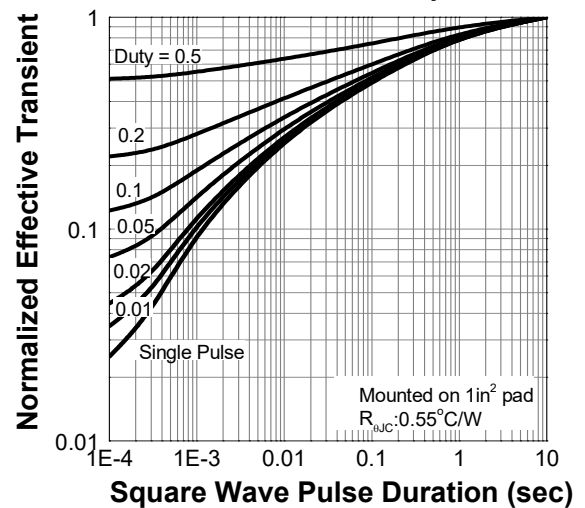
Electrical Characteristics ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	85	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=85V, V_{GS}=0V$	---	---	1	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	---	4	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=30A$	---	2.4	2.8	m Ω
Dynamic Characteristics ^⑤						
C_{iss}	Input Capacitance	$V_{DS}=40V,$ $V_{GS}=0V,$ Freq.=1MHz	---	6900	---	pF
C_{oss}	Output Capacitance		---	750	---	
C_{rss}	Reverse Transfer Capacitance		---	75	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DS}=40V, V_{GS}=10V,$ $I_D=30A, R_G=4.5\Omega$	---	23	---	nS
T_r	Turn-on Rise Time		---	68	---	
$T_{d(off)}$	Turn-off Delay Time		---	71	---	
T_f	Turn-off Fall Time		---	75	---	
Q_g	Total Gate Charge	$V_{DS}=40V, V_{GS}=10V,$ $I_D=30A$	---	110	---	nC
Q_{gs}	Gate-Source Charge		---	28	---	
Q_{gd}	Gate-Drain Charge		---	25	---	
Source-Drain Characteristics						
V_{SD}	Diode Forward Voltage	$I_S=30A, V_{GS}=0V$	---	---	1.3	V
t_{rr}	Reverse Recovery Time	$I_F=30A, V_{GS}=0V,$ $di_F/dt=100A/\mu s$	---	62	---	nS
Q_{rr}	Reverse Recovery Charge		---	83	---	nC

Note ④: Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).

Note ⑤: Guaranteed by design, not subject to production testing.

N-Channel Enhancement Mode MOSFET
Typical Characteristics


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Capacitance

Gate Charge

Power Capability

Current Capability

Safe Operating Area

Transient Thermal Impedance


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TO-220 Package Outline Data


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.350	4.650
A1	2.250	2.550
b	0.710	0.910
b1	1.170	1.400
c	0.330	0.650
c1	1.200	1.400
D	9.910	10.250
E	8.9500	9.750
E1	12.650	12.950
e	2.540 TYP.	
e1	4.980	5.180
F	2.650	2.950
H	7.900	8.100
h	0.000	0.300
L	12.700	13.500
L1	2.850	3.250
V	7.500 REF.	
Φ	3.400	3.800