

P-Channel Enhancement Mode MOSFET

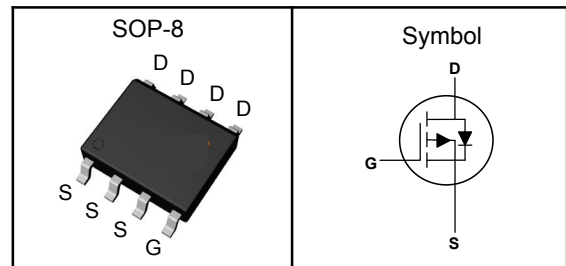
Features

- Low $R_{ds(on)}$ for low conduction loss
- Reliable and Rugged
- ROHS Compliant & Halogen-Free

Applications

- Power Management in Desktop Computer
- DC/DC Converters

Pin Description



V_{DSS}	-30	V
$R_{DS(ON)-Typ}$	5.5	m Ω
I_D	-16	A

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	P-Channel	Unit
V_{DSS}	Drain-Source Voltage	-30	V
V_{GSS}	Gate-Source Voltage	± 25	V
T_J	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}^{①}$	Pulse Drain Current Tested	-64	A
I_D	Continuous Drain Current	-16	A
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$ 3.1	W
E_{AS}	Single Pulse Avalanche Energy ₃ (L=0.3mH)	76	mJ

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}^{③}$	Thermal Resistance Junction-Ambient ₁	75	$^\circ\text{C}/\text{W}$
$R_{\theta JL}$	Thermal Resistance-Junction to Lead	24	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150 $^\circ\text{C}$.

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.



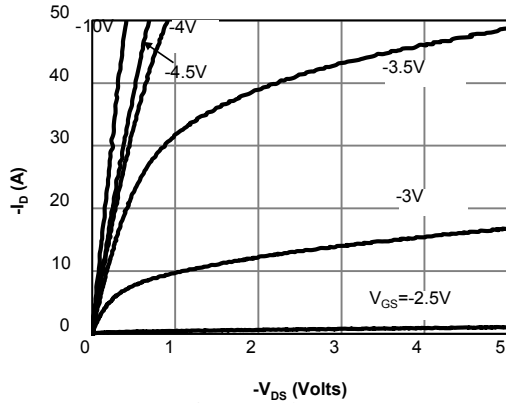
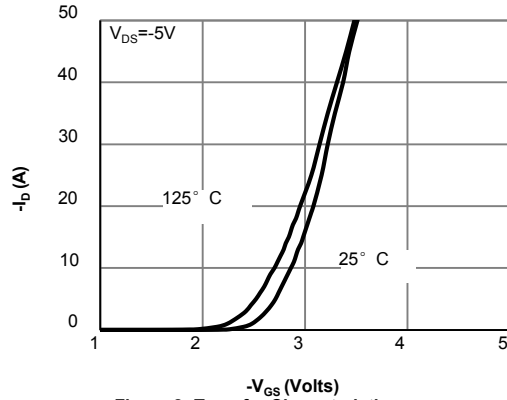
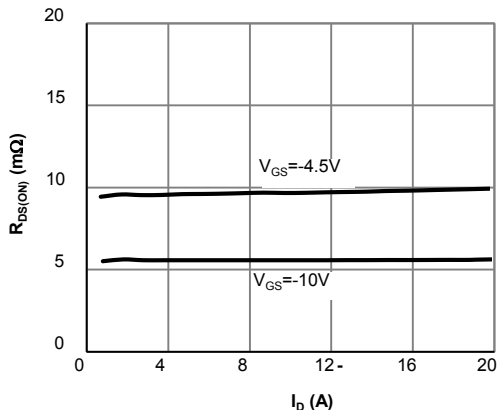
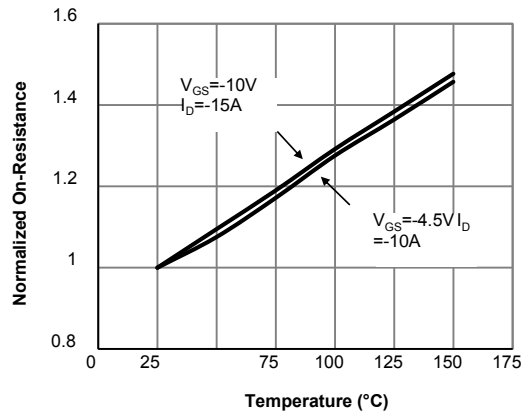
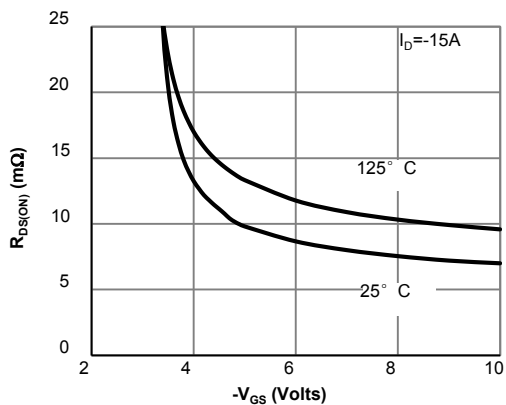
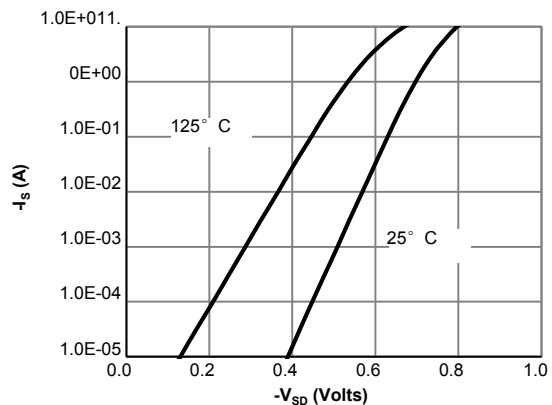
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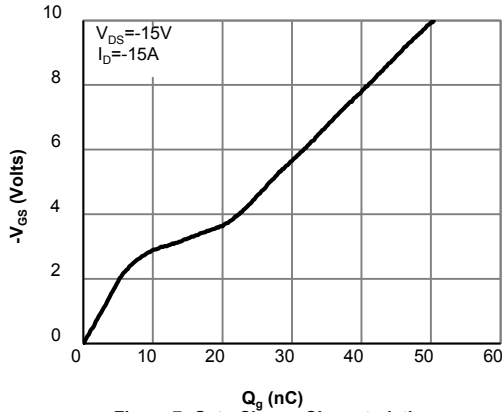
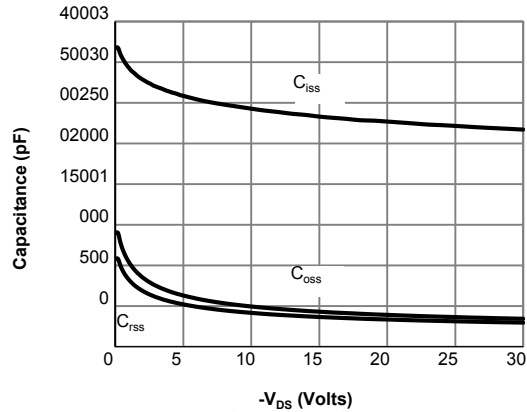
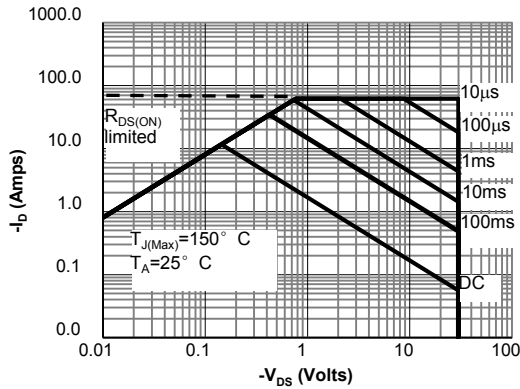
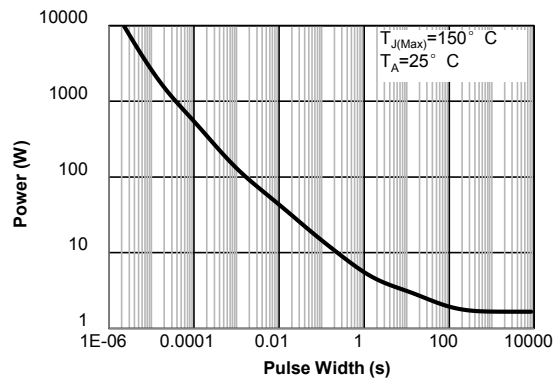
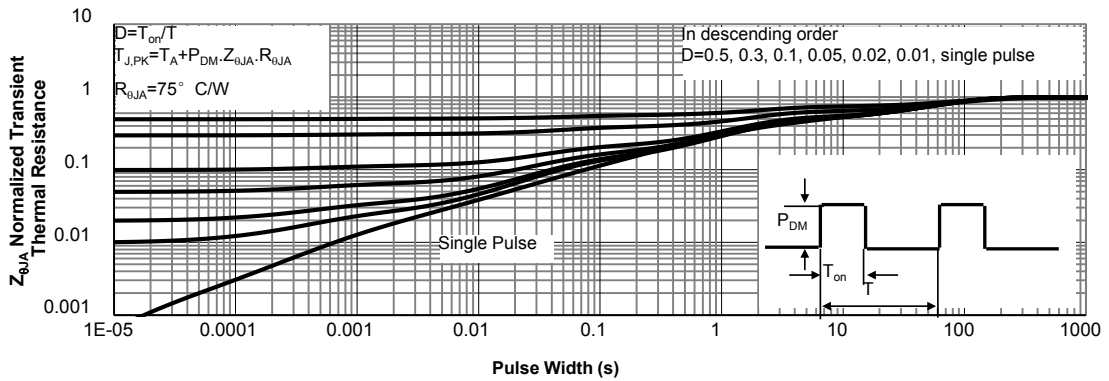
Electrical Characteristics ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

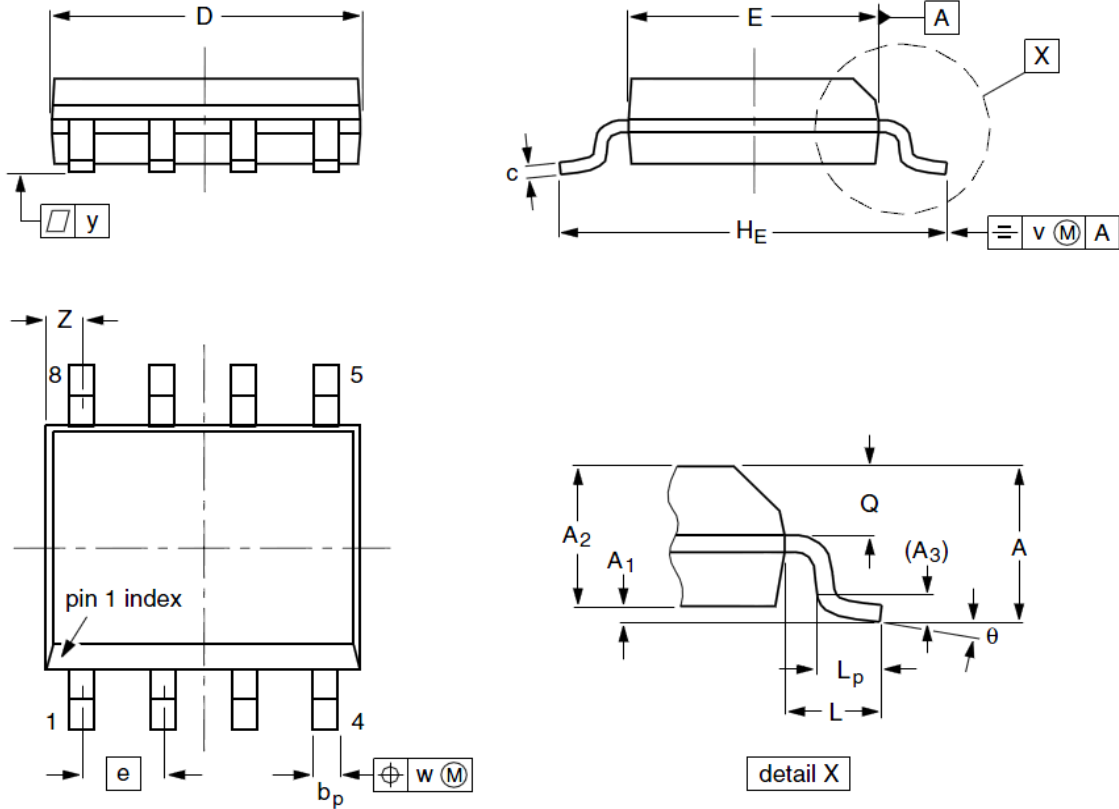
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Static Electrical Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-24V, V_{GS}=0V$	---	---	-1	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0	---	-2.7	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	---	---	± 100	nA
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=-10V, I_D=-15A$	---	5.5	7.2	$m\Omega$
		$V_{GS}=-4.5V, I_D=-10A$	---	9	12	$m\Omega$
Dynamic Characteristics ^⑤						
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=-15V, \text{Freq.}=1\text{MHz}$	---	3560	---	pF
C_{oss}	Output Capacitance		---	635	---	
C_{rss}	Reverse Transfer Capacitance		---	645	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DS}=-15V, V_{GS}=-10V, R_G=3\Omega, R_L=0.95\Omega$	---	12	---	nS
T_r	Turn-on Rise Time		---	12.5	---	
$T_{d(off)}$	Turn-off Delay Time		---	135	---	
T_f	Turn-off Fall Time		---	63	---	
Q_g	Total Gate Charge	$V_{DS}=-15V, V_{GS}=-10V, I_D=-16A$	---	50	---	nC
Q_{gs}	Gate-Source Charge		---	9	---	
Q_{gd}	Gate-Drain Charge		---	12	---	
Source-Drain Characteristics ($T_J=25^{\circ}\text{C}$)						
V_{SD}	Diode Forward Voltage _z	$V_{GS}=0V, I_S=-1A, T_J=25^{\circ}\text{C}$	---	-0.7	-1.0	V
t_{rr}	Reverse Recovery Time	$I_F=-16A, di/dt=500A/\mu s, T_J=25^{\circ}\text{C}$	---	32	---	nS
Q_{rr}	Reverse Recovery Charge		---	62	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).

Note ⑤ : Guaranteed by design, not subject to production testing.

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Typical Characteristics

Figure 1: On-Region Characteristics

Figure 2: Transfer Characteristics

Figure 3: On-Resistance vs. Drain Current and Gate Voltage

Figure 4: On-Resistance vs. Junction Temperature

Figure 5: On-Resistance vs. Gate-Source Voltage

Figure 6: Body-Diode Characteristics

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Figure 7: Gate-Charge Characteristics

Figure 8: Capacitance Characteristics

Figure 9: Maximum Forward Biased Safe Operating Area

Figure 10: Single Pulse Power Rating Junction-to-Ambient

Figure 11: Normalized Maximum Transient Thermal Impedance

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SOP-8 Package Outline Dimensions


Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	1.35	1.55	1.75	A₁	0.10	0.18	0.25
A₂	1.25	1.45	1.65	A₃	--	0.25	--
b_p	0.36	0.42	0.51	c	0.19	0.22	0.25
D	4.70	4.92	5.10	E	3.80	3.90	4.00
e	--	1.27	--	H_E	5.80	6.00	6.20
L	--	1.05	--	L_p	0.40	0.68	1.00
Q	0.60	0.65	0.73	v	--	0.25	--
w	--	0.25	--	y	--	0.10	--
Z	0.30	0.50	0.70	θ	0°		8°