

# N-Channel Enhancement Mode MOSFET

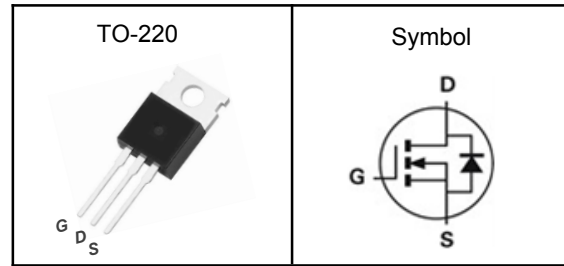
## Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant
- 100% UIS and Rg Tested

## Applications

- Power Management in Desktop Computer
- DC/DC Converters

## Pin Description



$V_{DSS}$	100	V
$R_{DS(ON)-Typ}$	3.1	m $\Omega$
$I_D$	140	A

## Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	N-Channel	Unit
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$E_{AS}$	Single Pulse Avalanche Energy <sup>③</sup>	367	mJ
$I_{DM}^{①}$	Pulse Drain Current Tested	560	A
$I_D$	Continuous Drain Current	$T_C=25^\circ\text{C}$	A
$P_D$	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	W

## Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62.5	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>①</sup>	0.85	$^\circ\text{C}/\text{W}$

Note ① : Max. current is limited by bonding wire.

Note ② : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$ .

Note ③ : Surface Mounted on  $1\text{in}^2$  FR-4 board with 1oz.



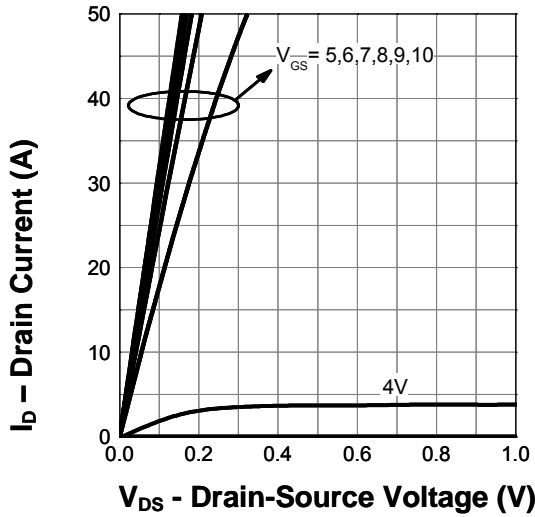
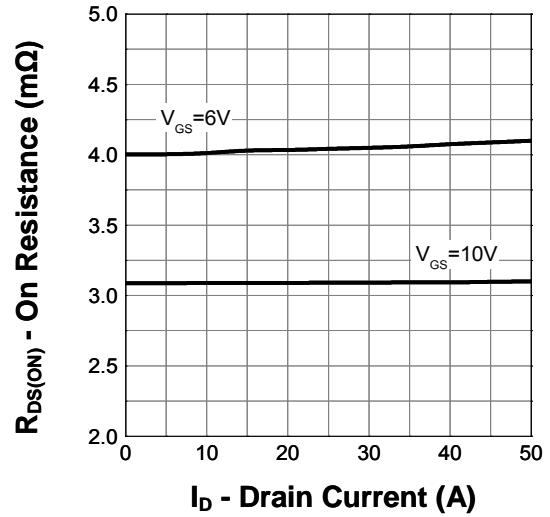
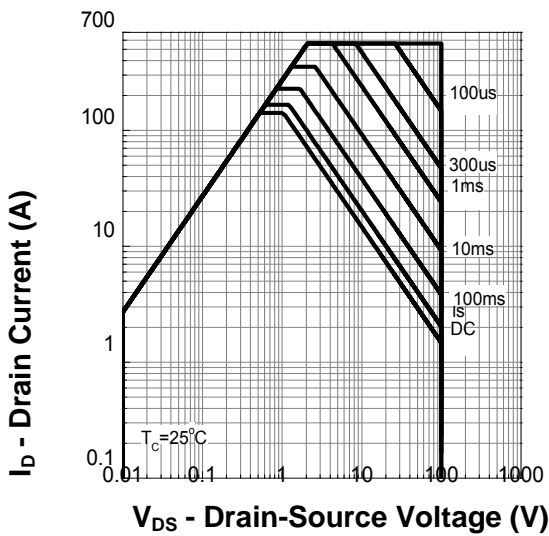
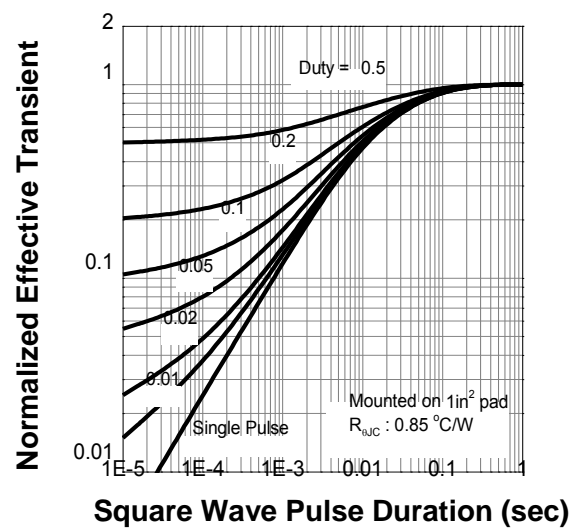
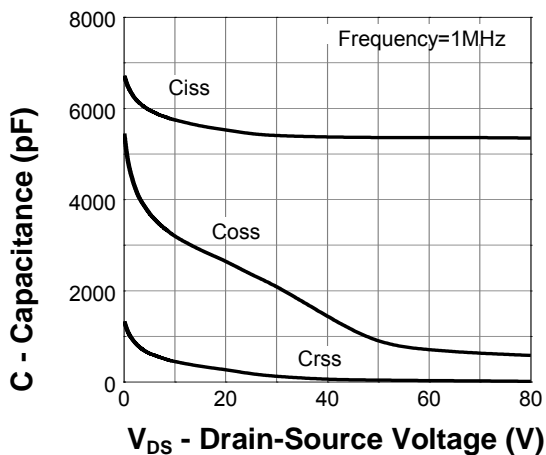
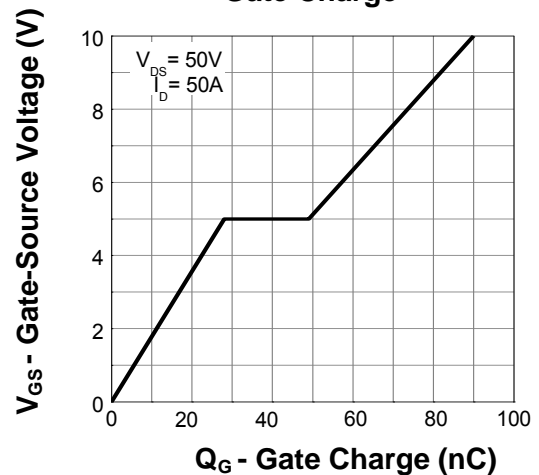
**N-Channel Enhancement Mode MOSFET**

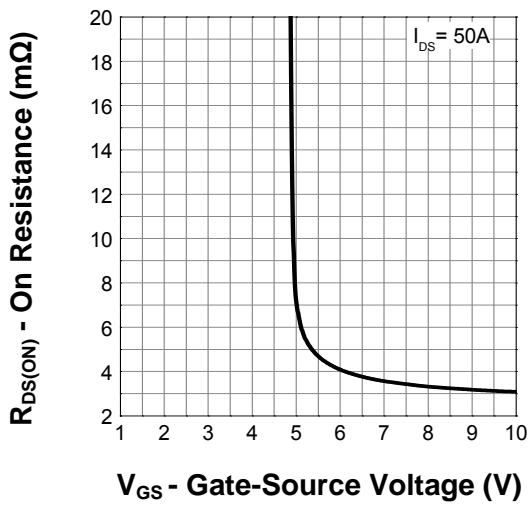
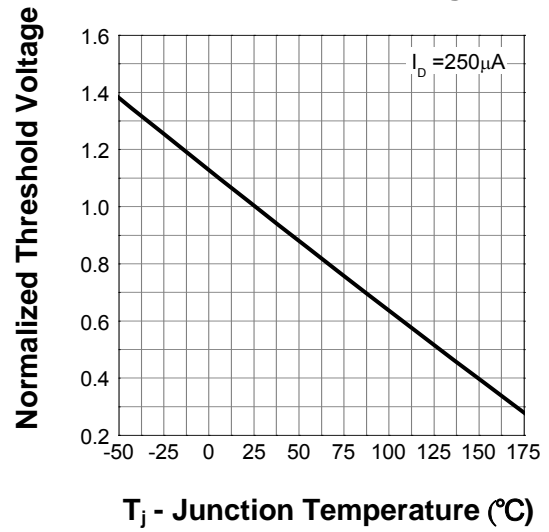
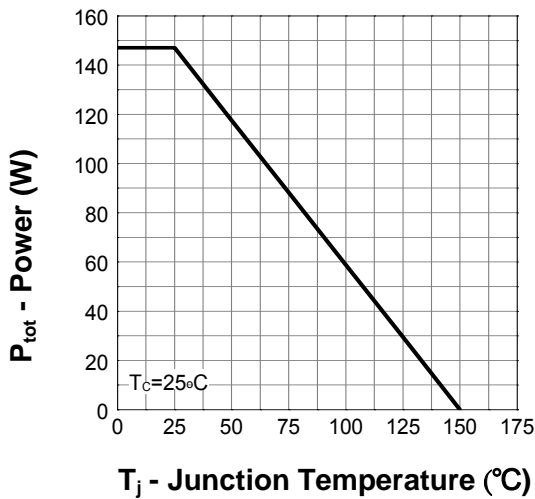
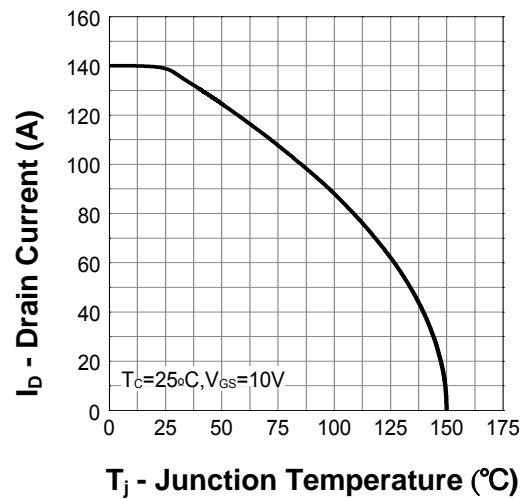
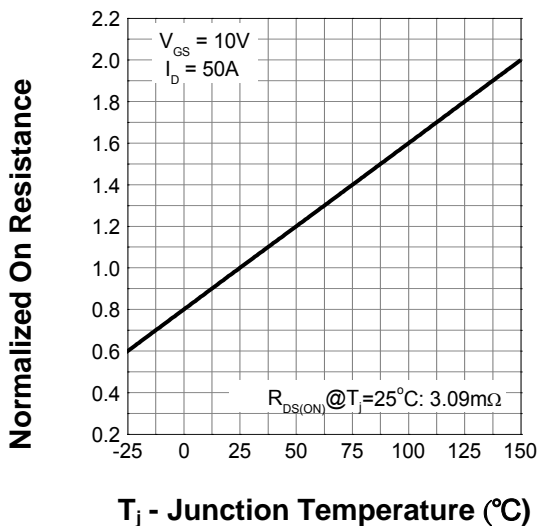
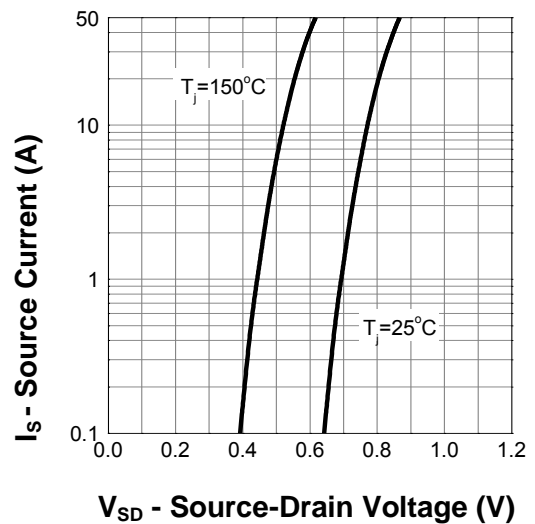
**Electrical Characteristics** ( $T_J=25^{\circ}\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Static Electrical Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250mA$	100	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=80V, V_{GS}=0V$	---	---	1	$\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	---	4.0	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$R_{DS(on)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=50A$	---	3.1	3.6	$m\Omega$
		$V_{GS}=6V, I_D=30A$	---	4.1	5	$m\Omega$
<b>Dynamic Characteristics</b> <sup>⑤</sup>						
$C_{iss}$	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=50V,$ Freq.=1MHz	---	5363	---	pF
$C_{oss}$	Output Capacitance		---	824	---	
$C_{rss}$	Reverse Transfer Capacitance		---	38	---	
$T_{d(on)}$	Turn-on Delay Time	$V_{DS} = 50 V, V_{GEN} = 10 V,$ $R_G = 3.9 \Omega, R_L = 1 \Omega,$ $I_D = 50 A$	---	19	---	nS
$T_r$	Turn-on Rise Time		---	85	---	
$T_{d(off)}$	Turn-off Delay Time		---	56	---	
$T_f$	Turn-off Fall Time		---	97	---	
$Q_g$	Total Gate Charge	$V_{DS}=50V, V_{GS}=10V,$ $I_D=50A$	---	90	---	nC
$Q_{gs}$	Gate-Source Charge		---	28	---	
$Q_{gd}$	Gate-Drain Charge		---	21	---	
<b>Source-Drain Characteristics</b> ( $T_J=25^{\circ}\text{C}$ )						
$V_{SD}$	Diode Forward Voltage <sub>z</sub>	$I_{SD}=50A, T_J=25^{\circ}\text{C}$	---	---	1.3	V
$t_{rr}$	Reverse Recovery Time	$I_{SD}=50A,$ $di/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	89	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	141	---	nC

Note ④ : Pulse test (pulse width $\leq 300\mu s$ , duty cycle $\leq 2\%$ ).

Note ⑤ : Guaranteed by design, not subject to production testing.

**N-Channel Enhancement Mode MOSFET**
**Typical Characteristics**
**Output Characteristics**

**Drain-Source On Resistance**

**Safe Operation Area**

**Thermal Transient Impedance**

**Capacitance**

**Gate Charge**


**N-Channel Enhancement Mode MOSFET**
**Transfer Characteristics**

**Gate Threshold Voltage**

**Power Capability**

**Current Capability**

**Drain-Source On Resistance**

**Body Diode Characteristics**


**N-Channel Enhancement Mode MOSFET**
**TO-220 Package Outline Data**


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.350	4.650
A1	2.250	2.550
b	0.710	0.910
b1	1.170	1.400
c	0.330	0.650
c1	1.200	1.400
D	9.910	10.250
E	8.9500	9.750
E1	12.650	12.950
e	2.540 TYP.	
e1	4.980	5.180
F	2.650	2.950
H	7.900	8.100
h	0.000	0.300
L	12.700	13.500
L1	2.850	3.250
V	7.500 REF.	
Φ	3.400	3.800